BLOCK DIAGRAM

SANYO

COLOUR VIDEO CASSETTE RECORDER

VTC 5350

TABLE OF CONTENTS

1. VIDEO CIRCUIT 1
2. SERVO CIRCUIT 17
3. SYSTEM CONTROL CIRCUIT 22
4. AUDIO CIRCUIT 31
5. TIMER CIRCUIT 34

1. VIDEO CIRCUIT

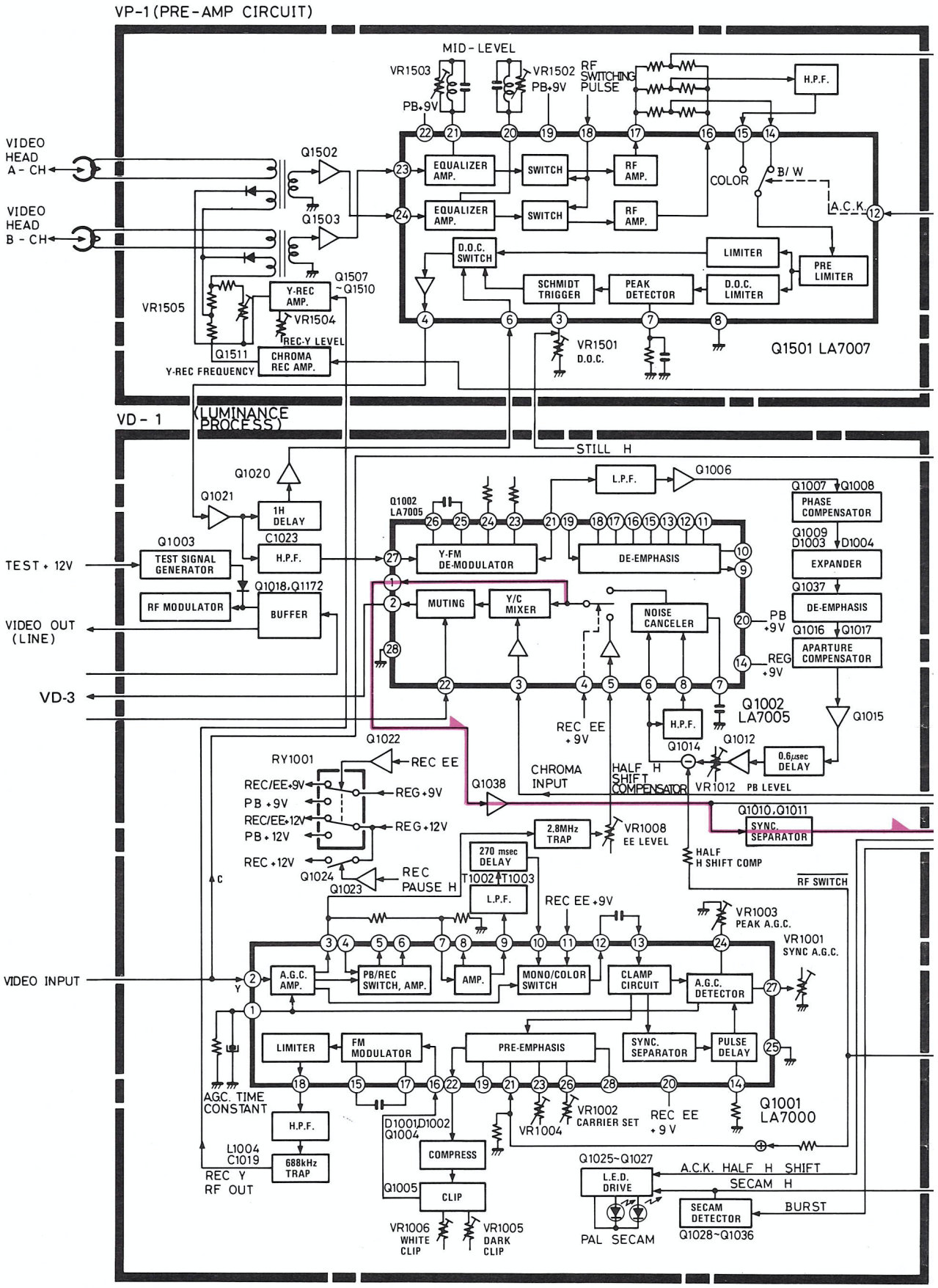
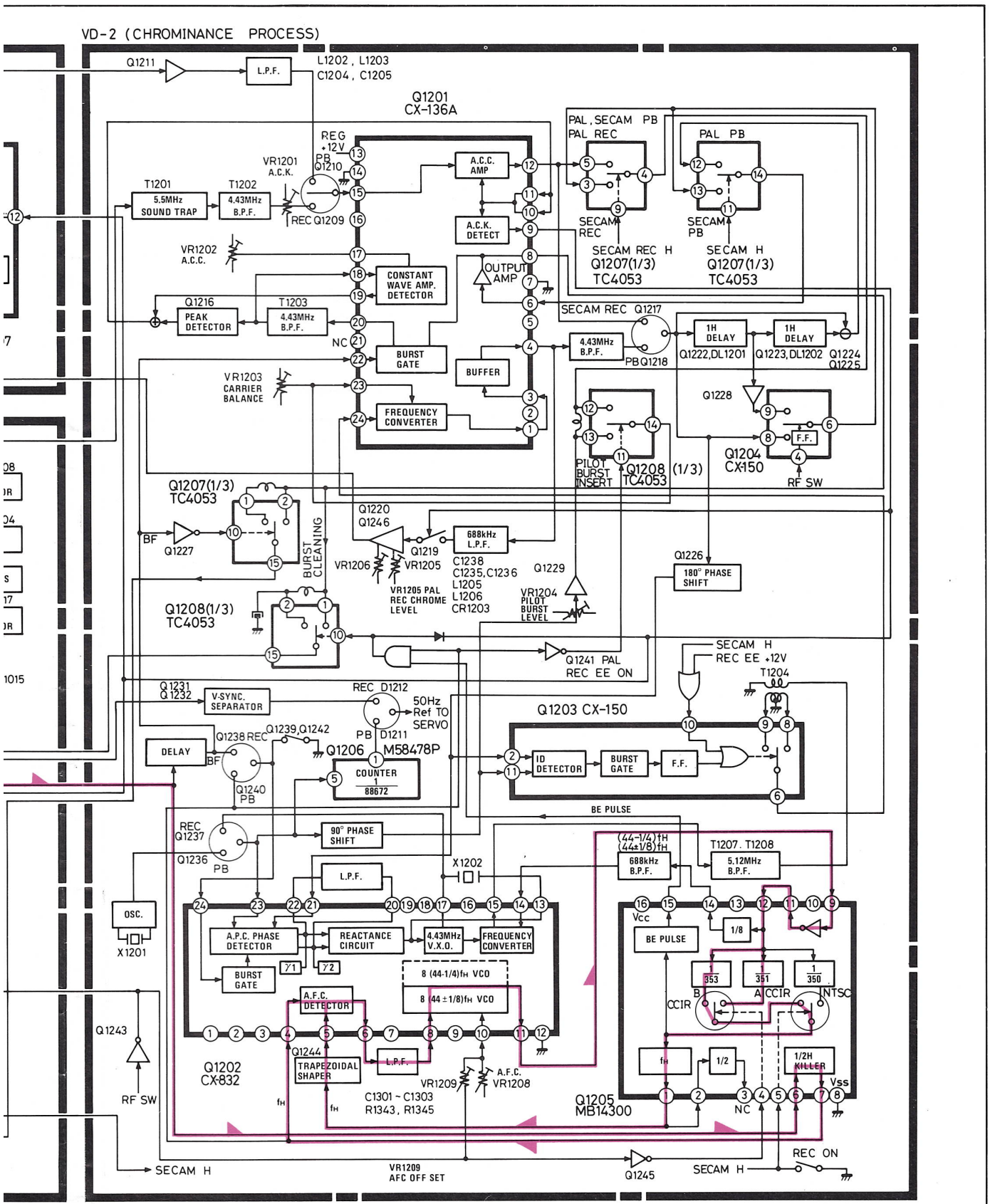


Fig. 1-1 Video circuit block diagr



lock diagram (PAL AFC REC/P.B mode)

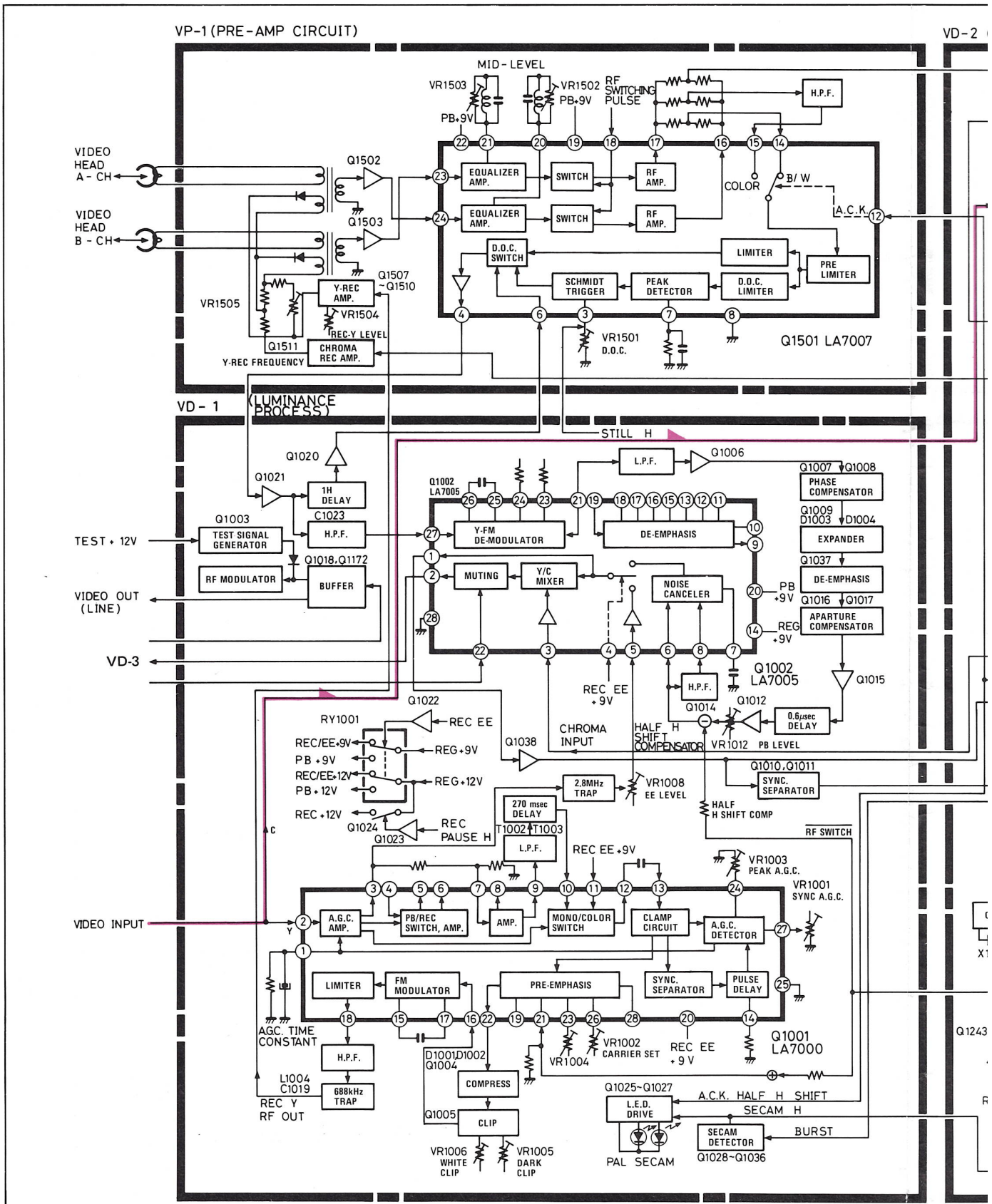
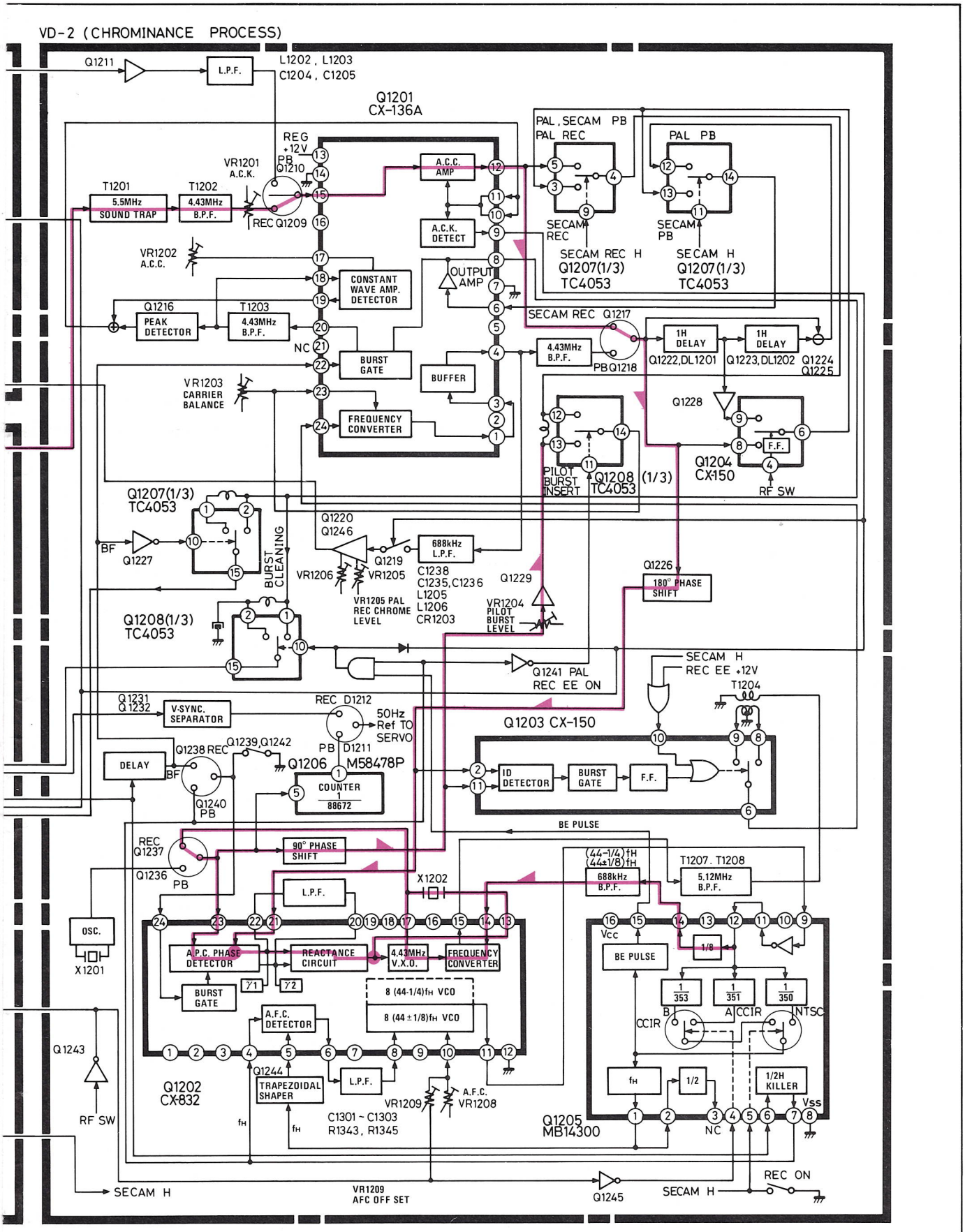


Fig. 1-2 Video circuit block diagram



:k diagram (PAL APC REC mode)

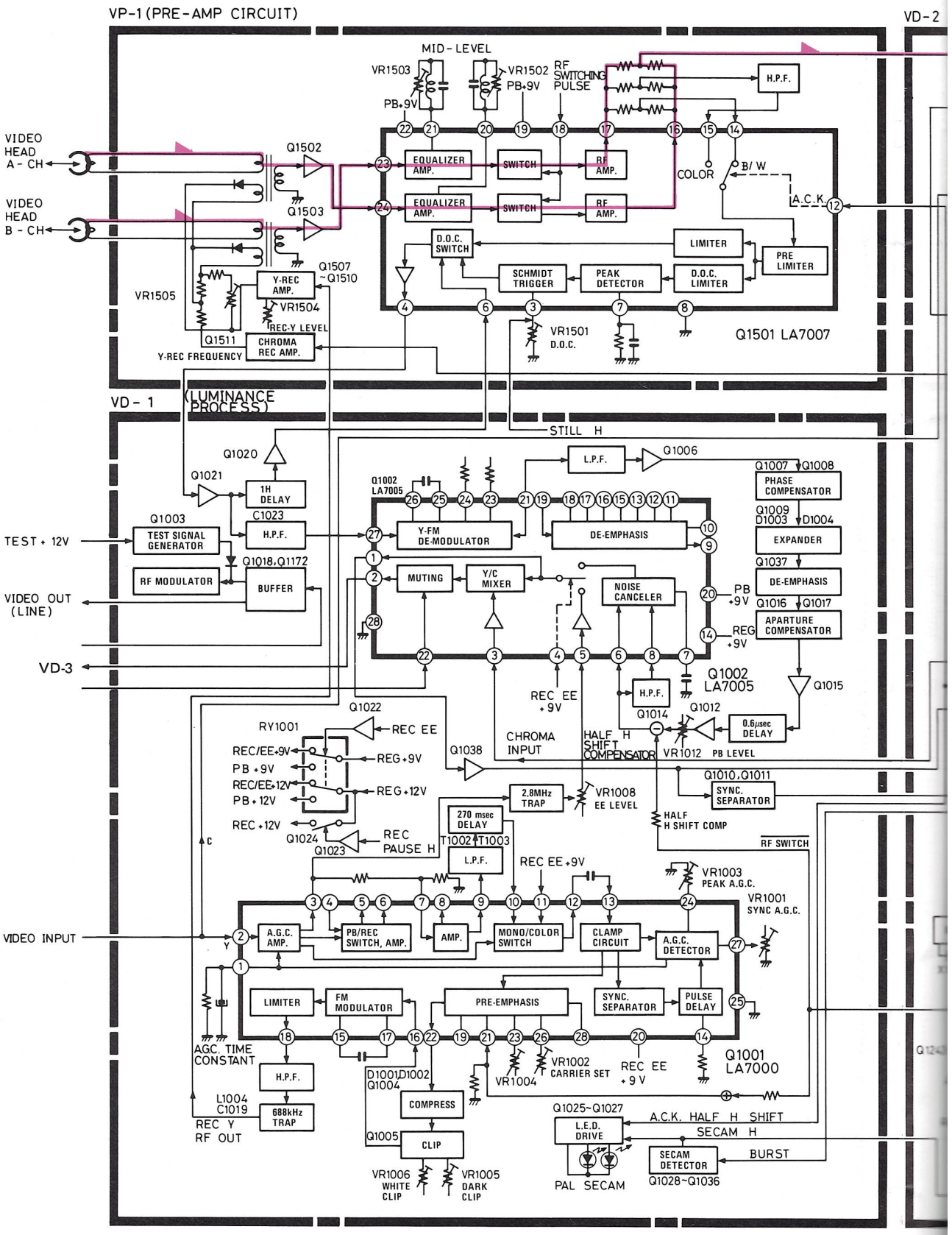
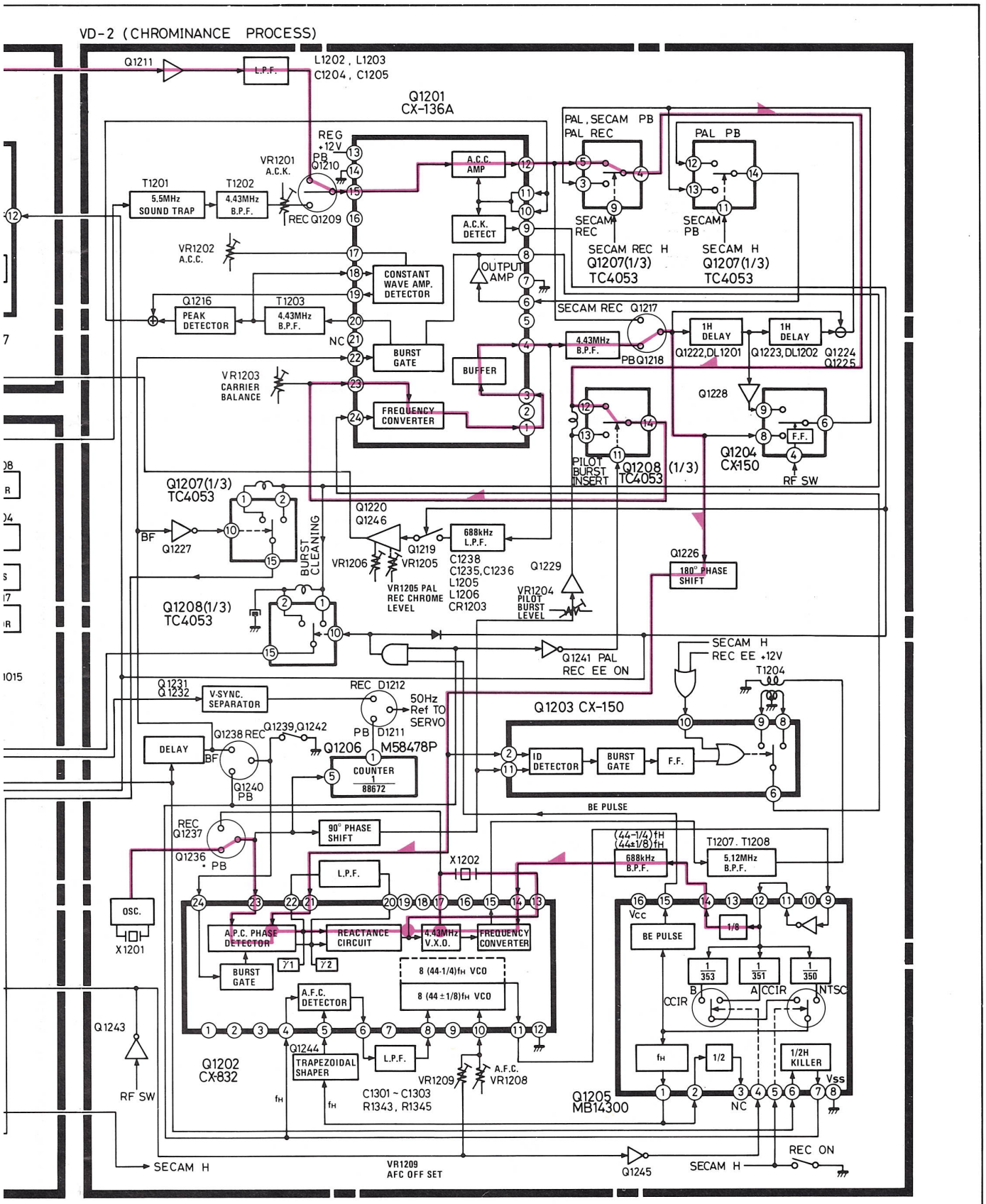


Fig. 1-3 Video circuit block diagram



t block diagram (PAL APC P.B mode)

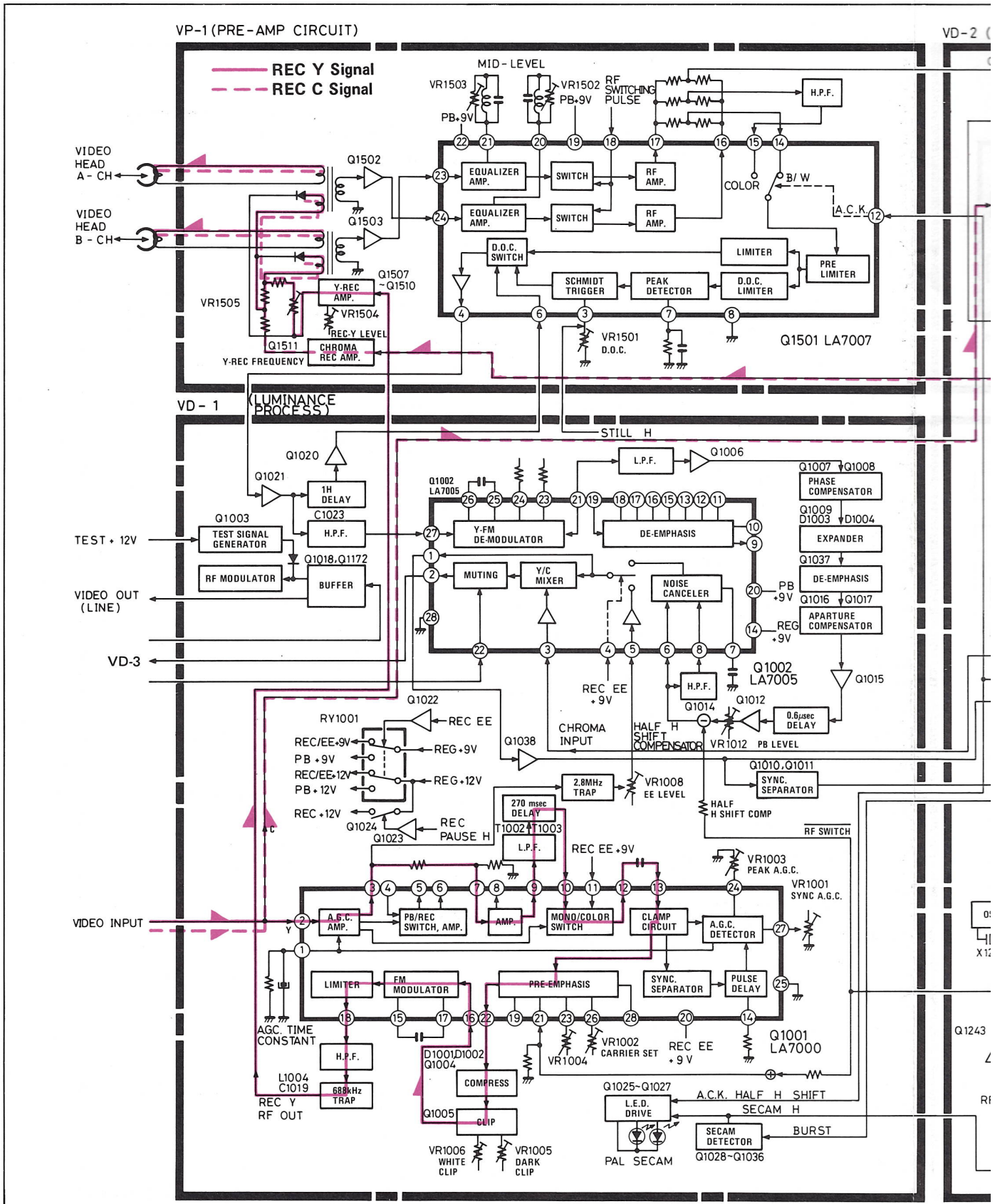
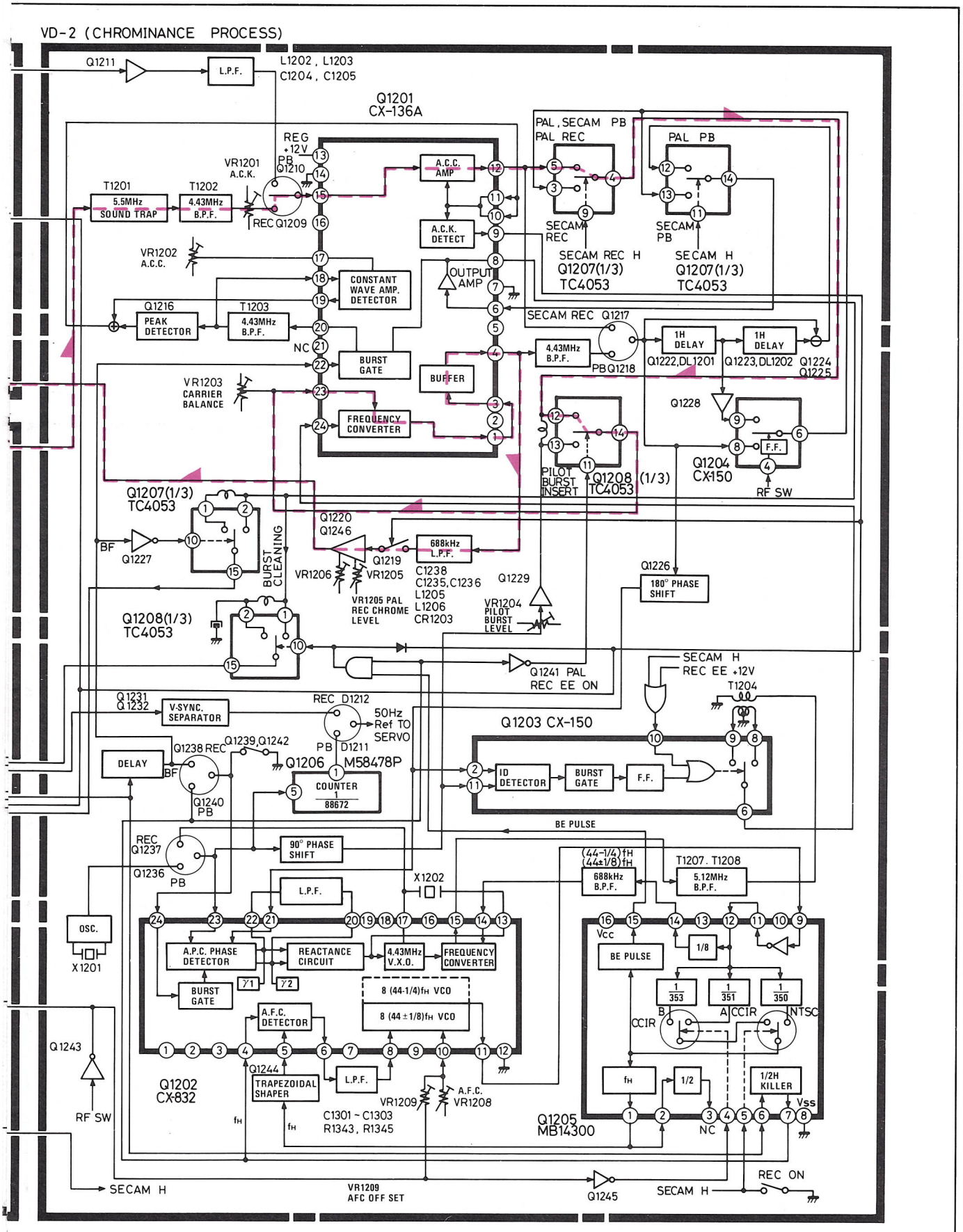


Fig. 1-4 Video circuit block diagram (PAL



agram (PAL Y/C signal REC mode)

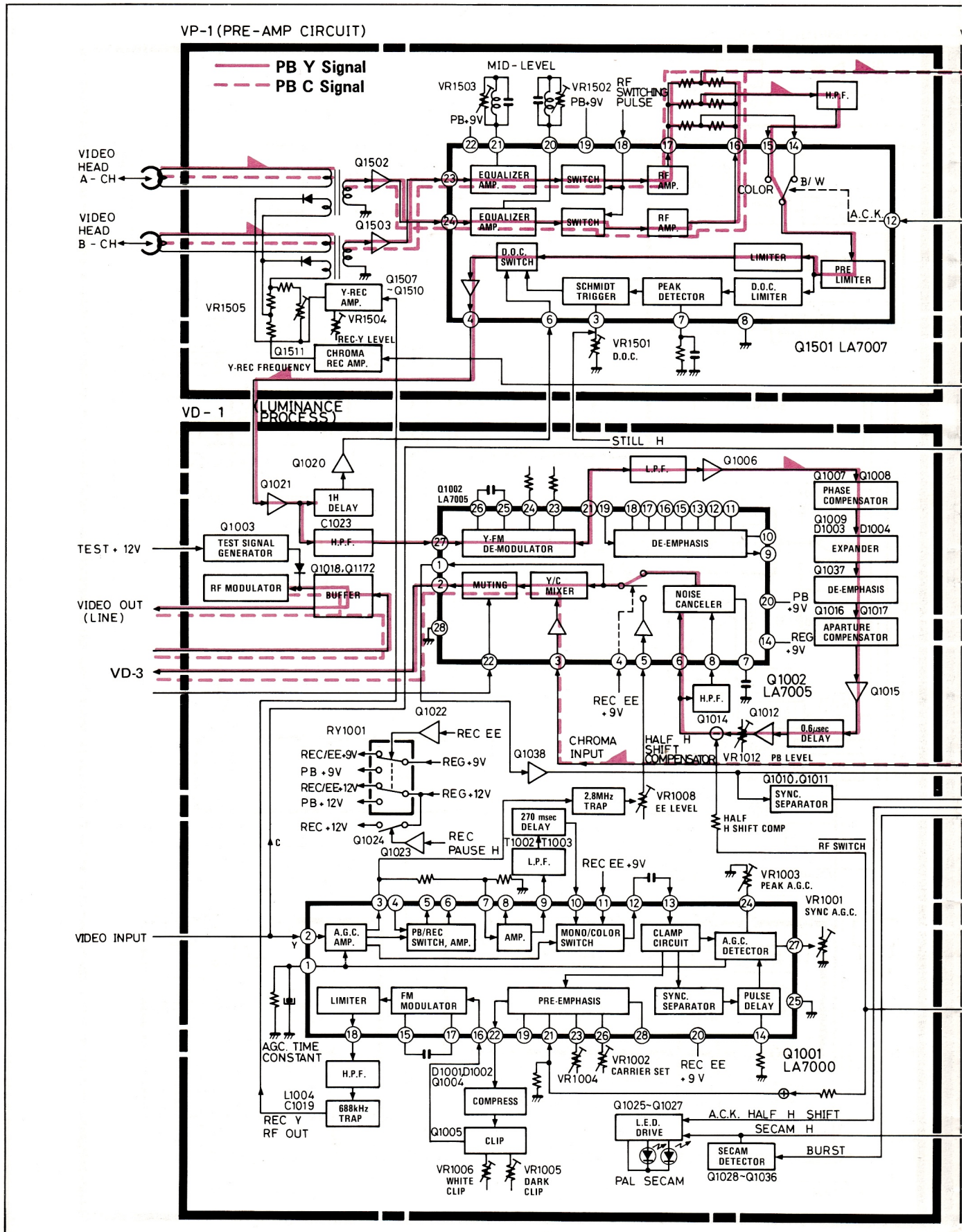
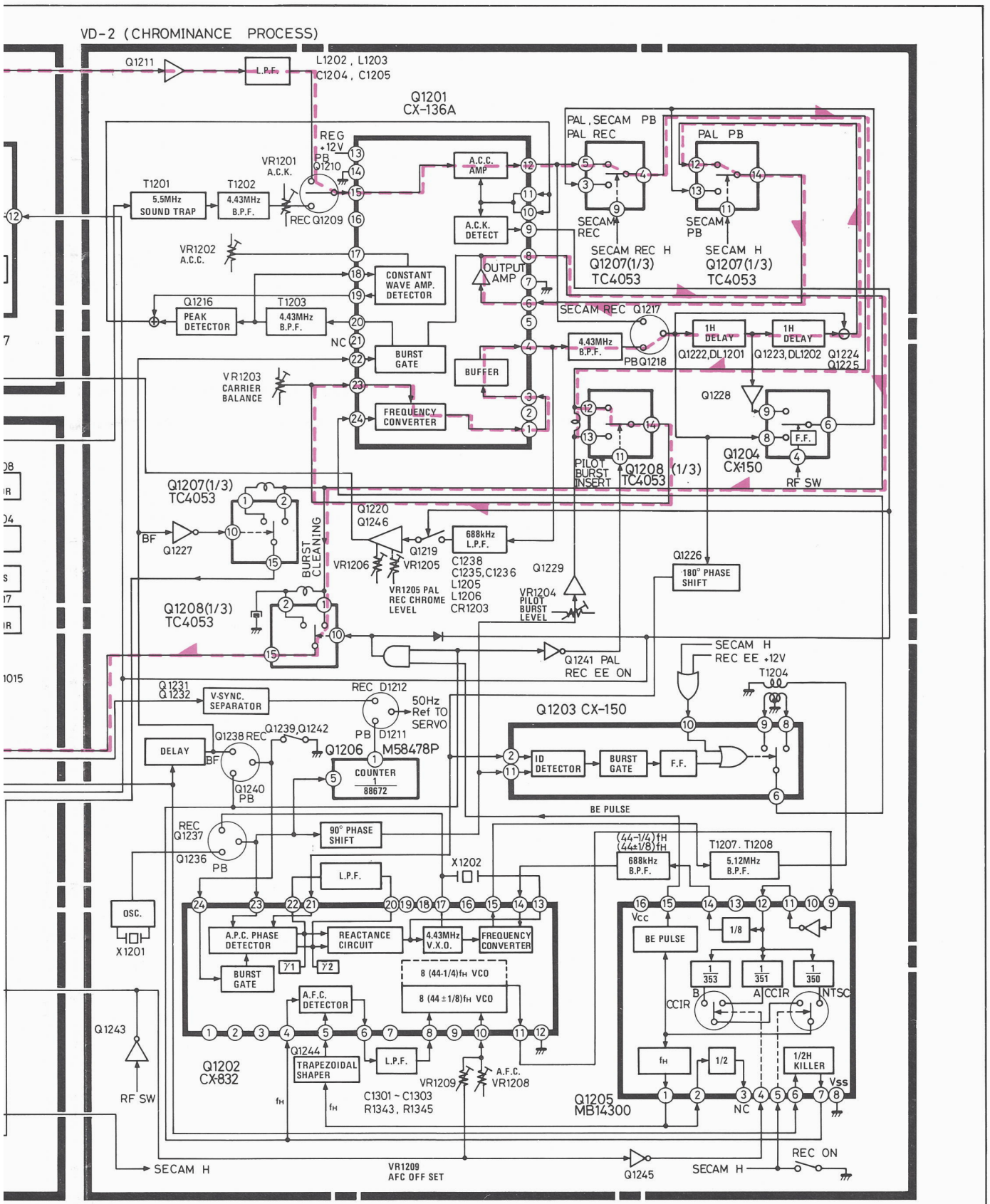


Fig. 1-5 Video circuit block diagra



lock diagram (PAL Y/C signal P.B mode)

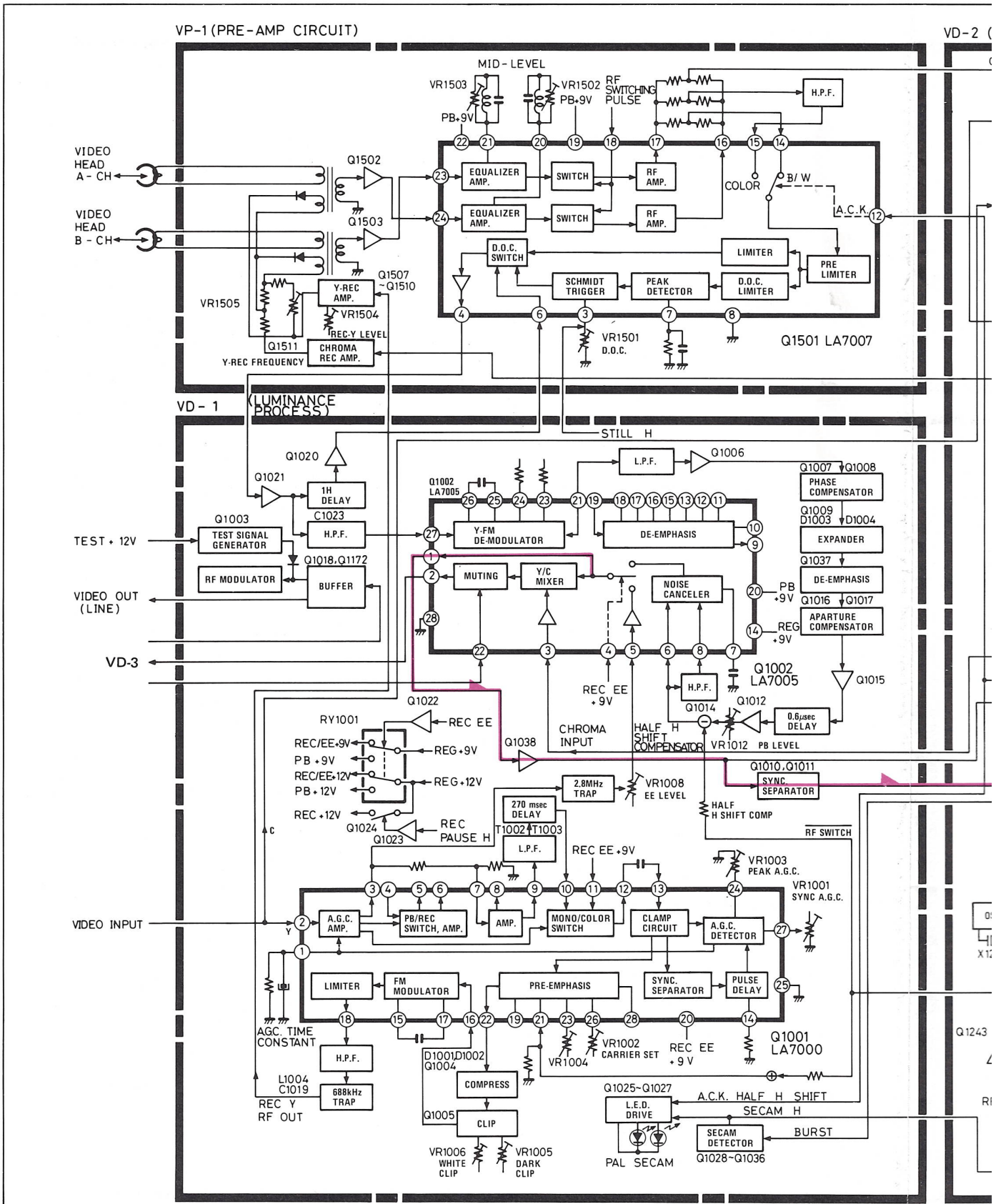
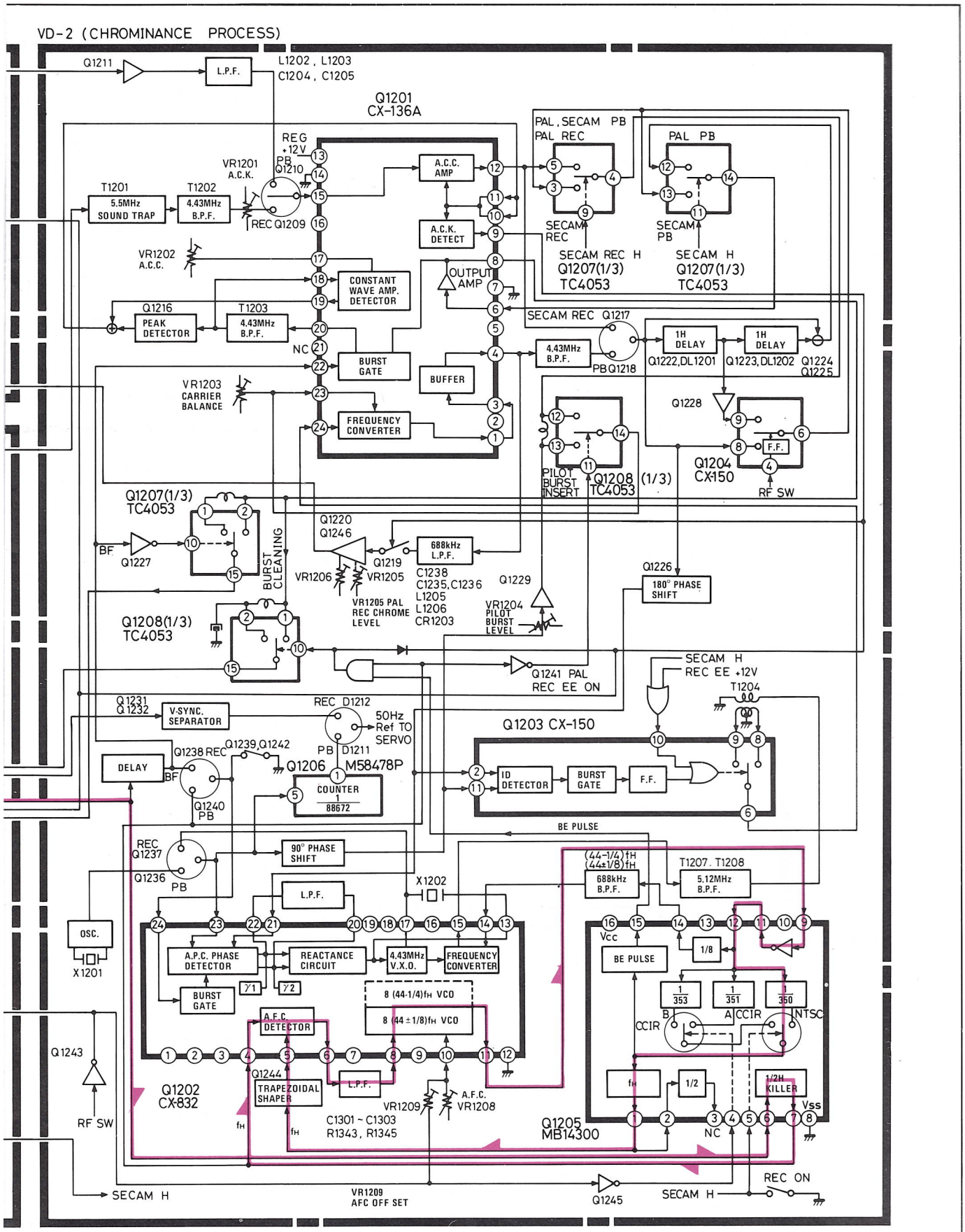


Fig. 1-6 Video circuit block diagram (SEC



gram (SECAM AFC REC/P.B mode)

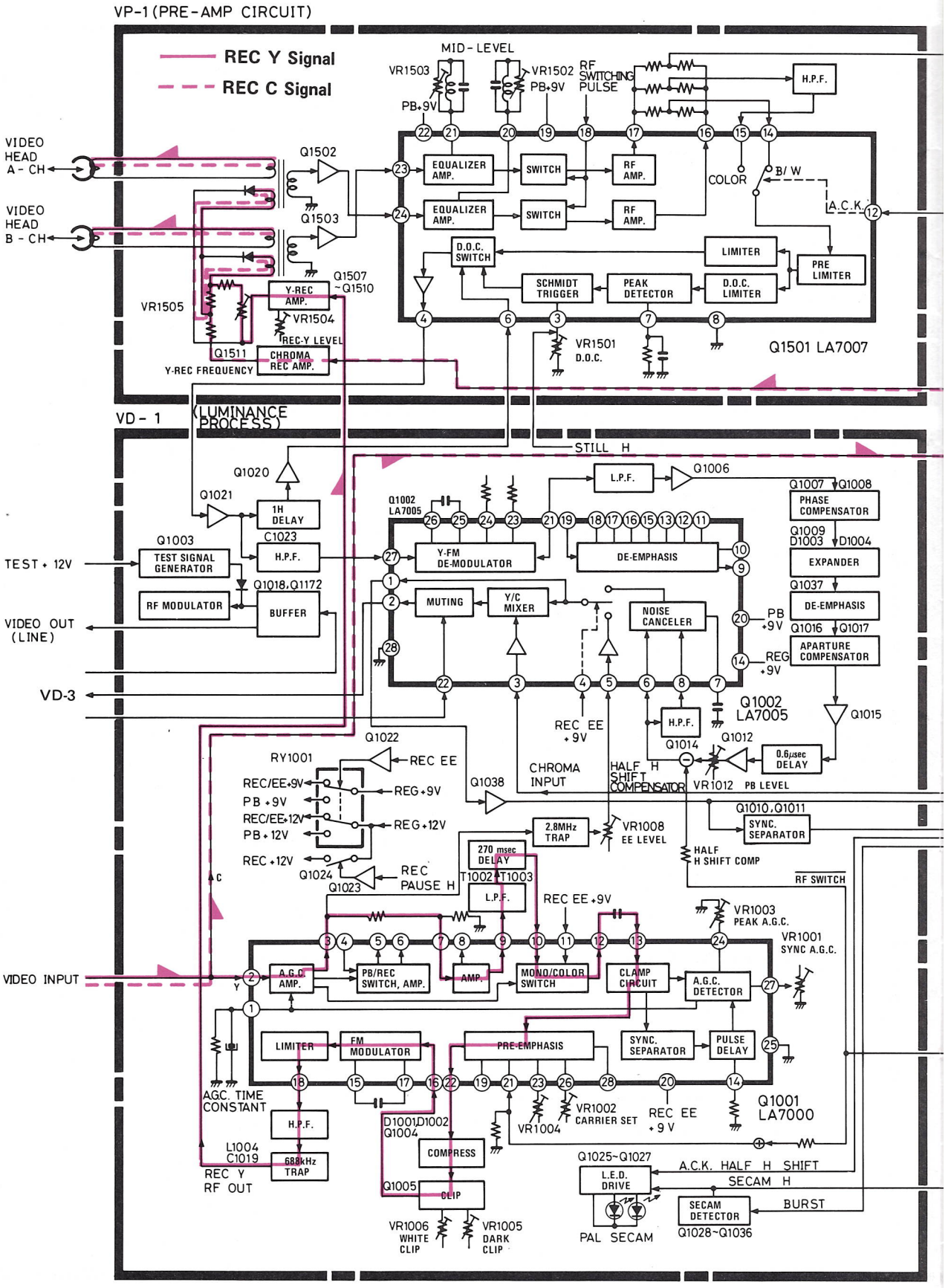
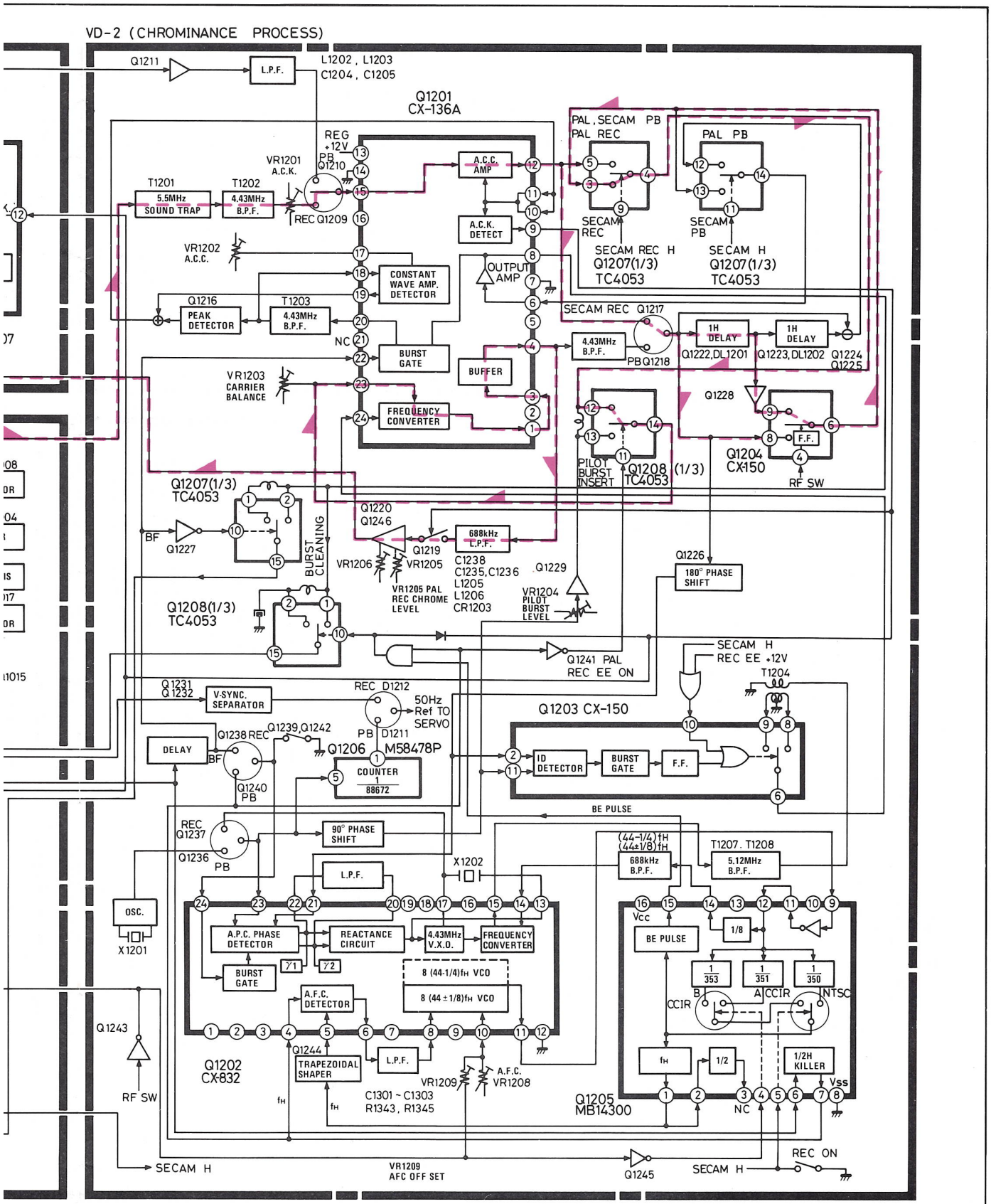


Fig. 1-7 Video circuit block diagram



ck diagram (SECAM Y/C signal REC mode)

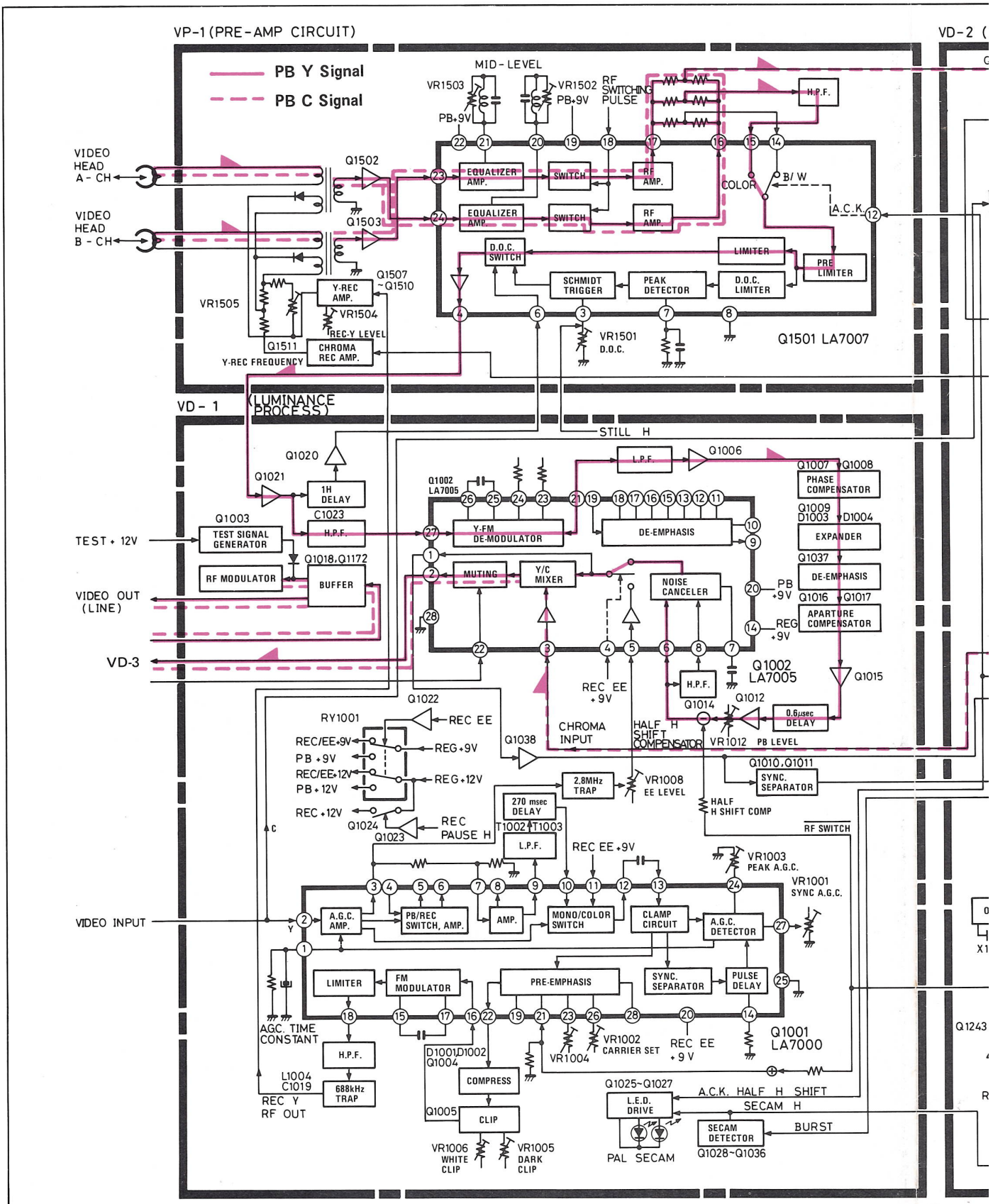
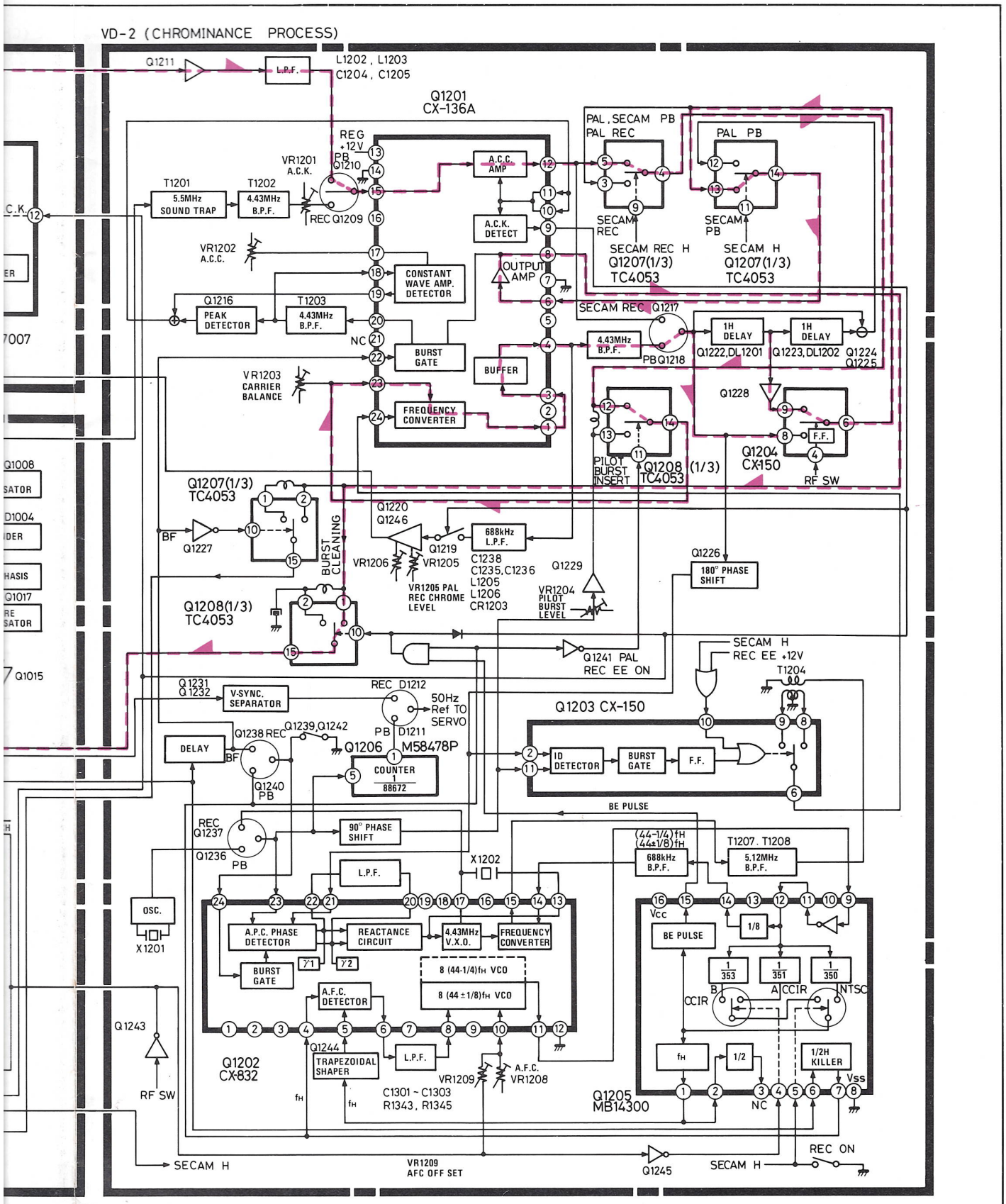


Fig. 1-8 Video circuit block diagram (SEC



block diagram (SECAM Y/C signal P.B mode)

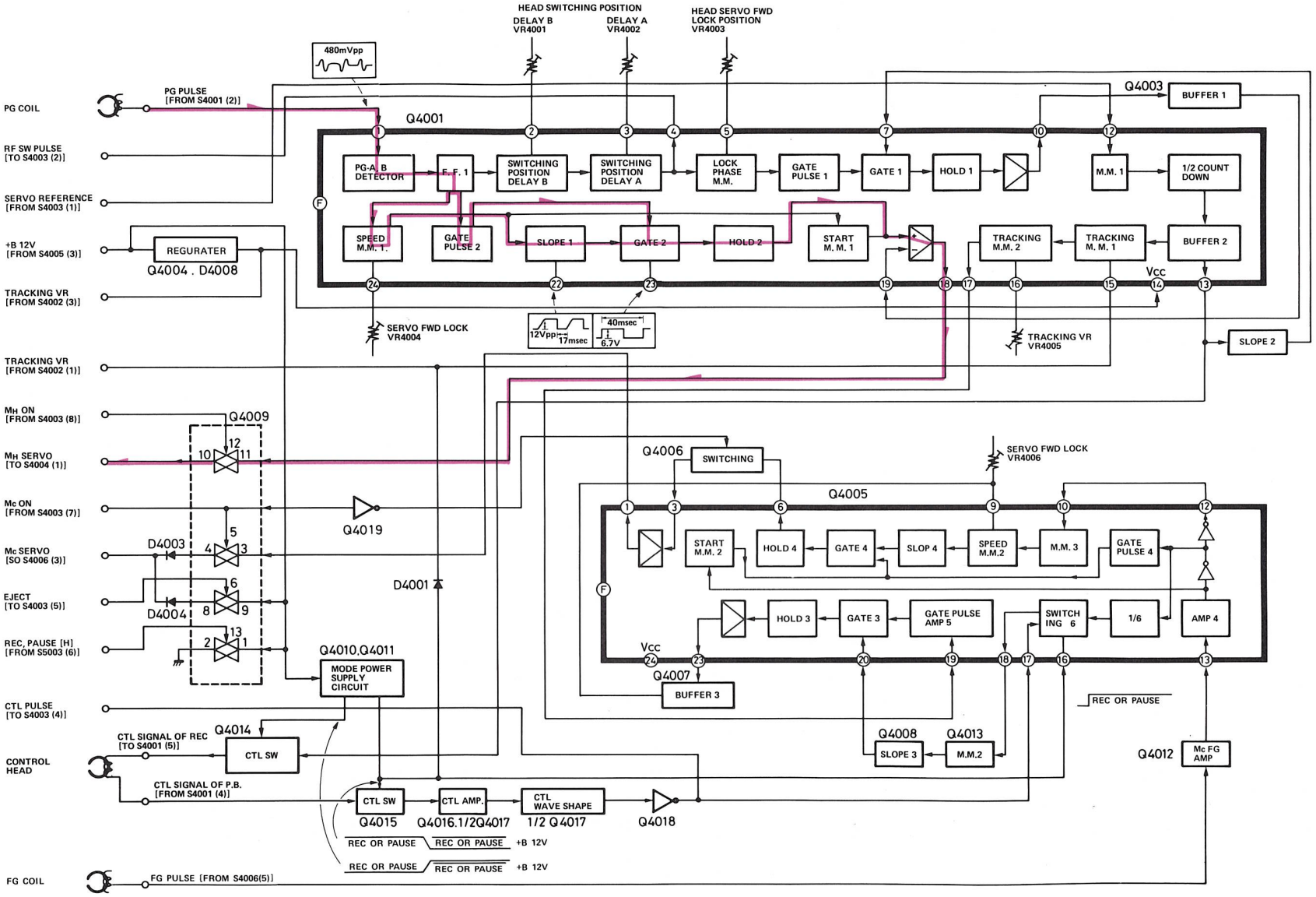


Fig. 2-1 Servo circuit block diagram (Drum Motor speed control)

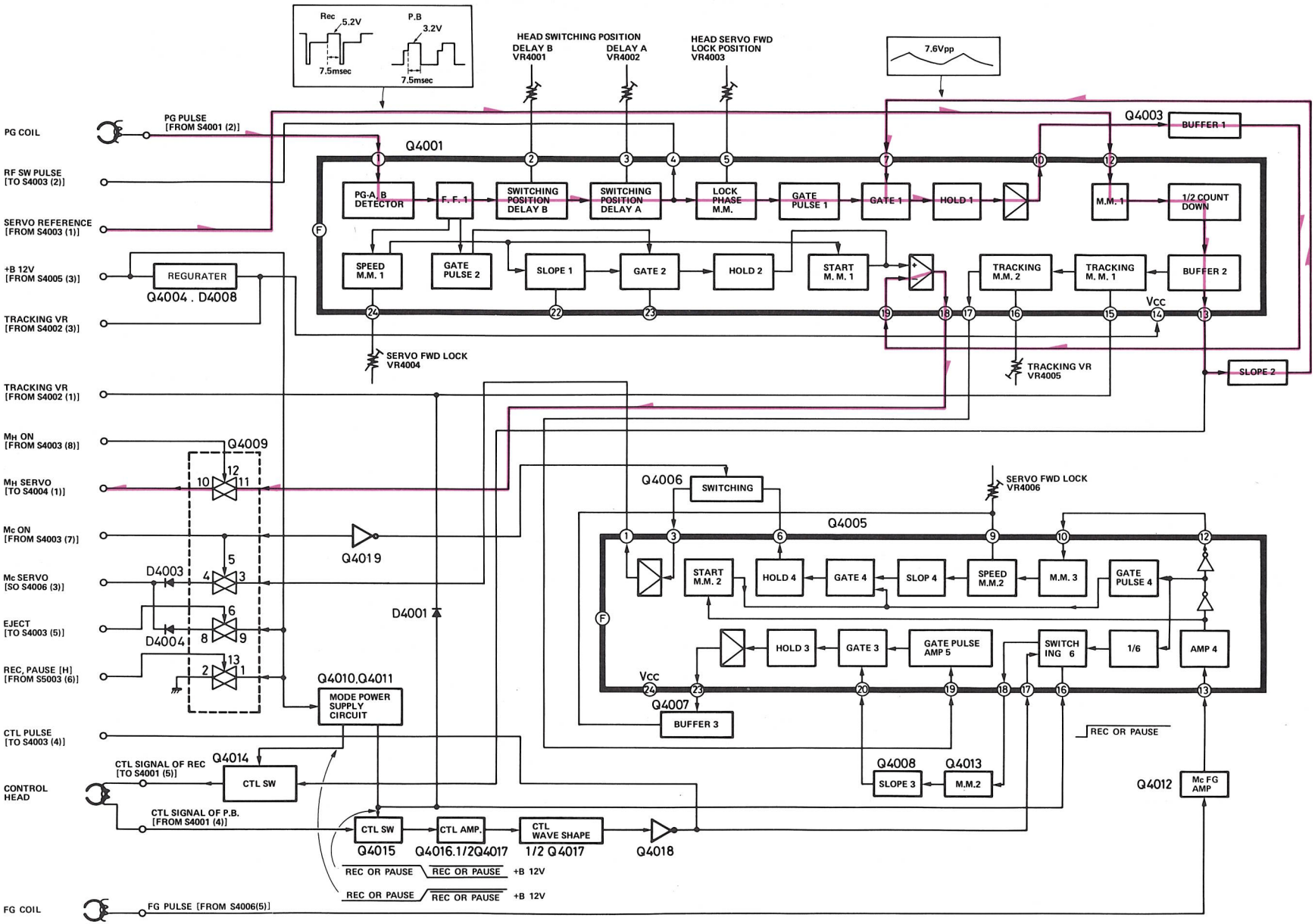


Fig. 2-2 Servo circuit block diagram (Drum Motor phase control)

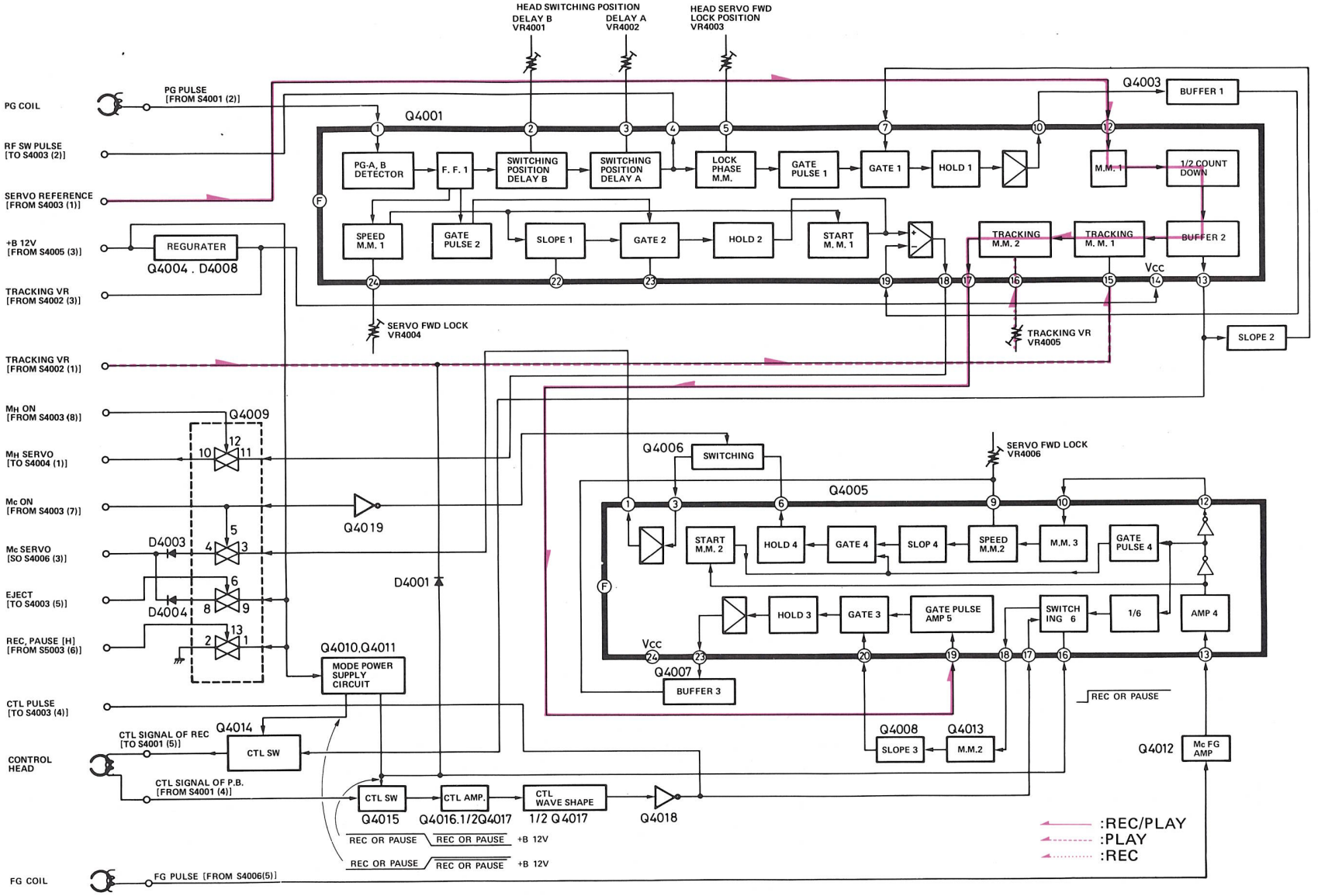


Fig. 2-3 Servo circuit block diagram (Delay of reference signal)

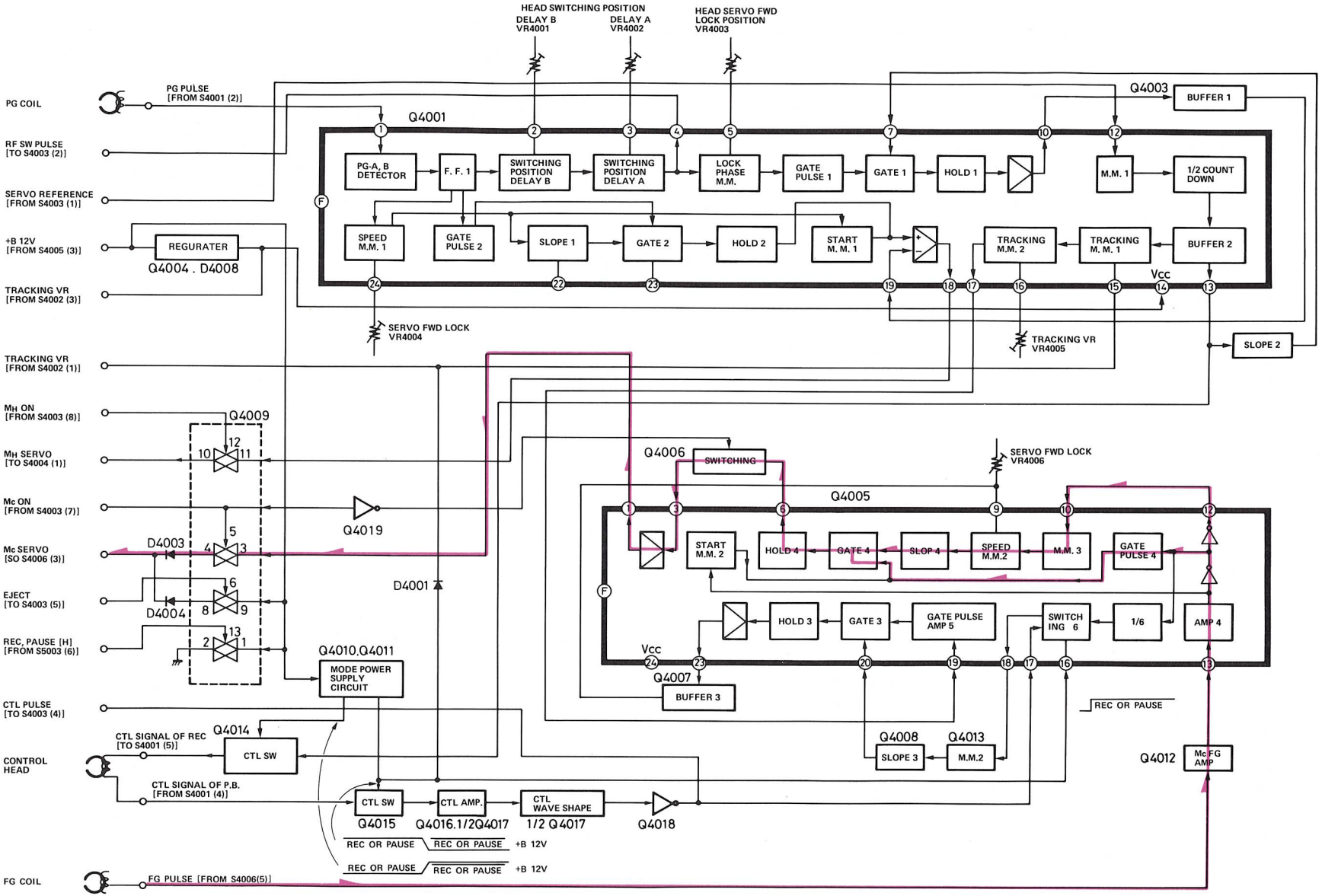


Fig. 2-4 Servo circuit block diagram (Capstan Motor speed control)

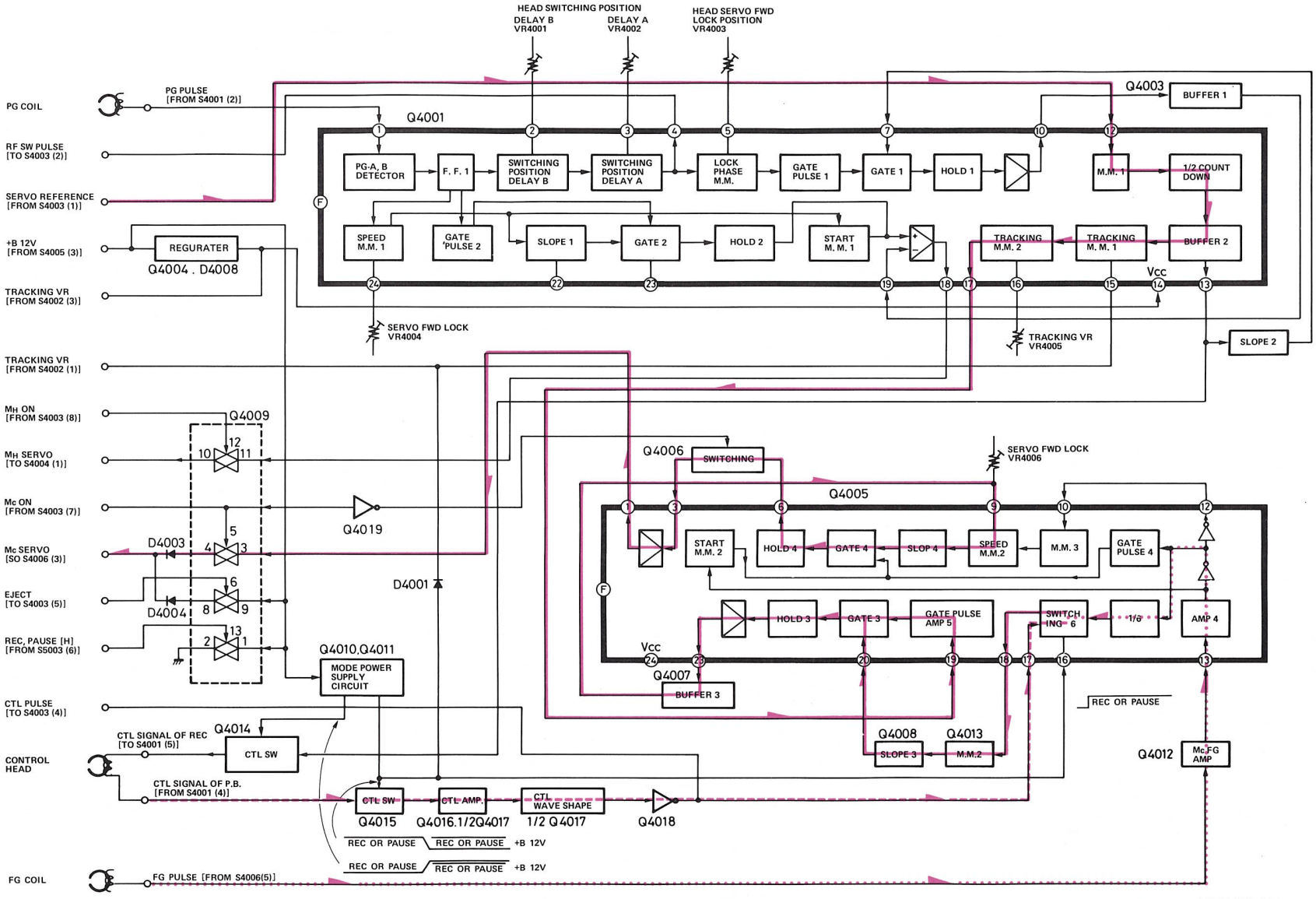


Fig. 2-5 Servo circuit block diagram (Capstan Motor phase control)

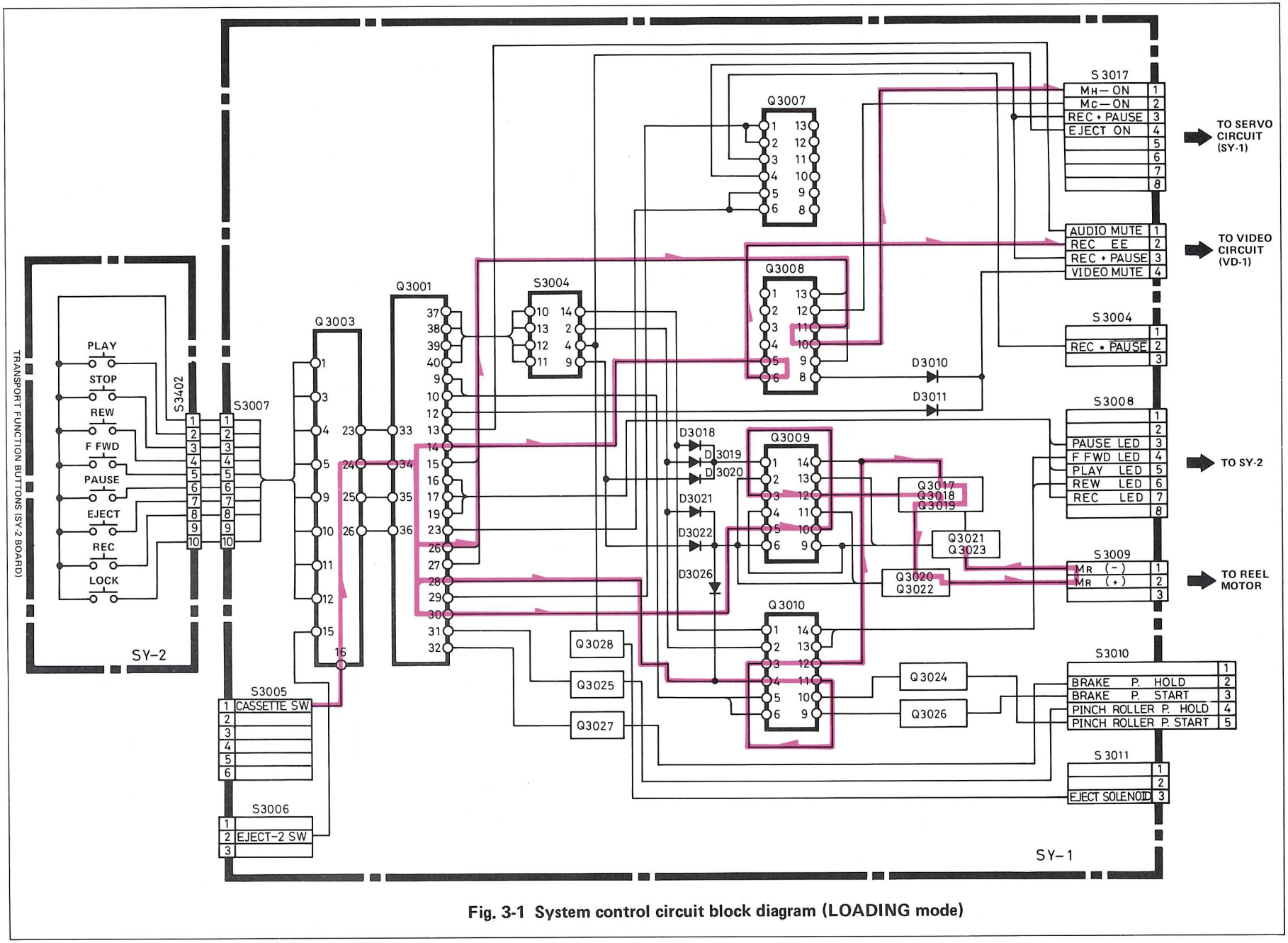


Fig. 3-1 System control circuit block diagram (LOADING mode)

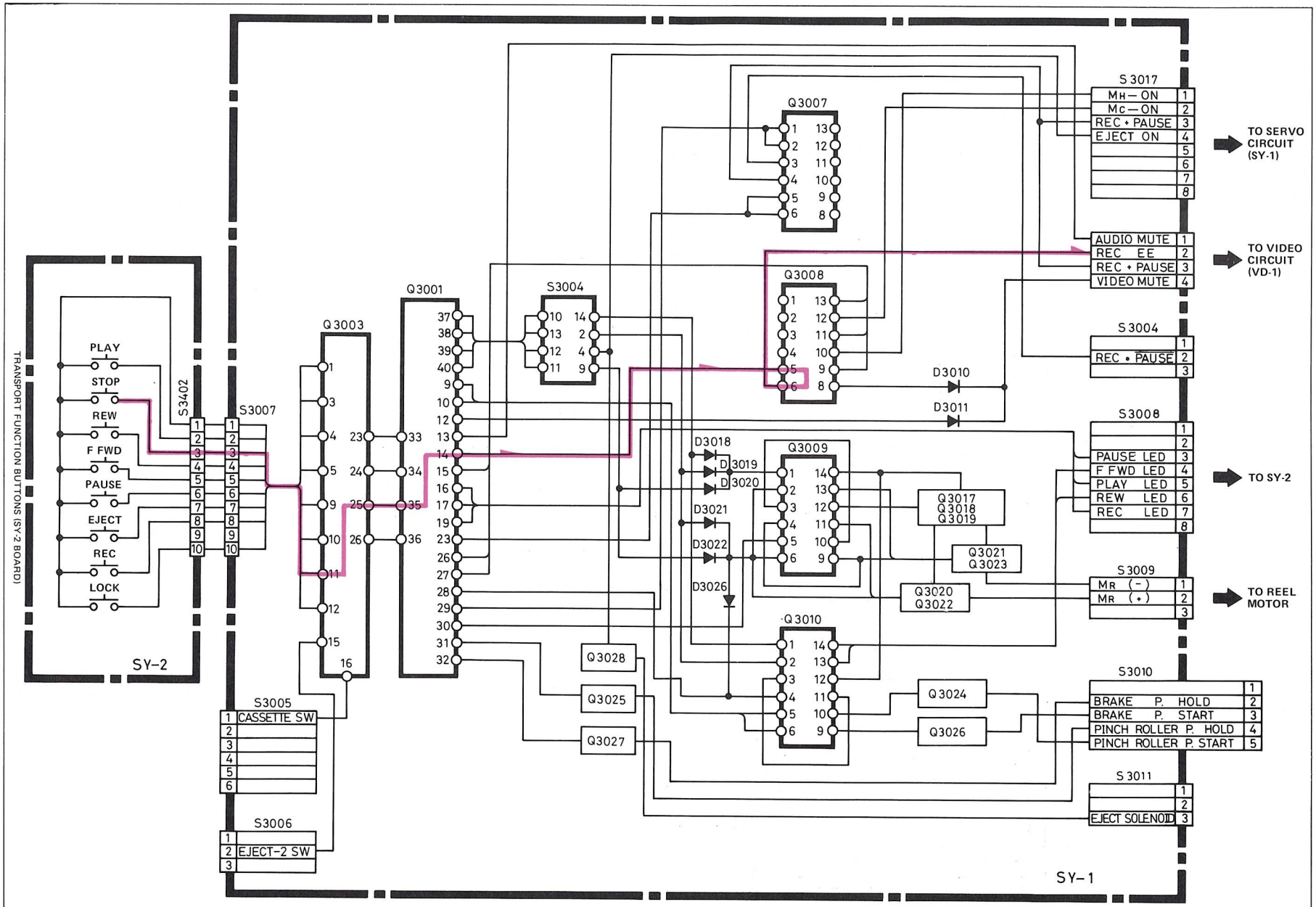


Fig. 3-2 System control circuit block diagram (STOP mode)

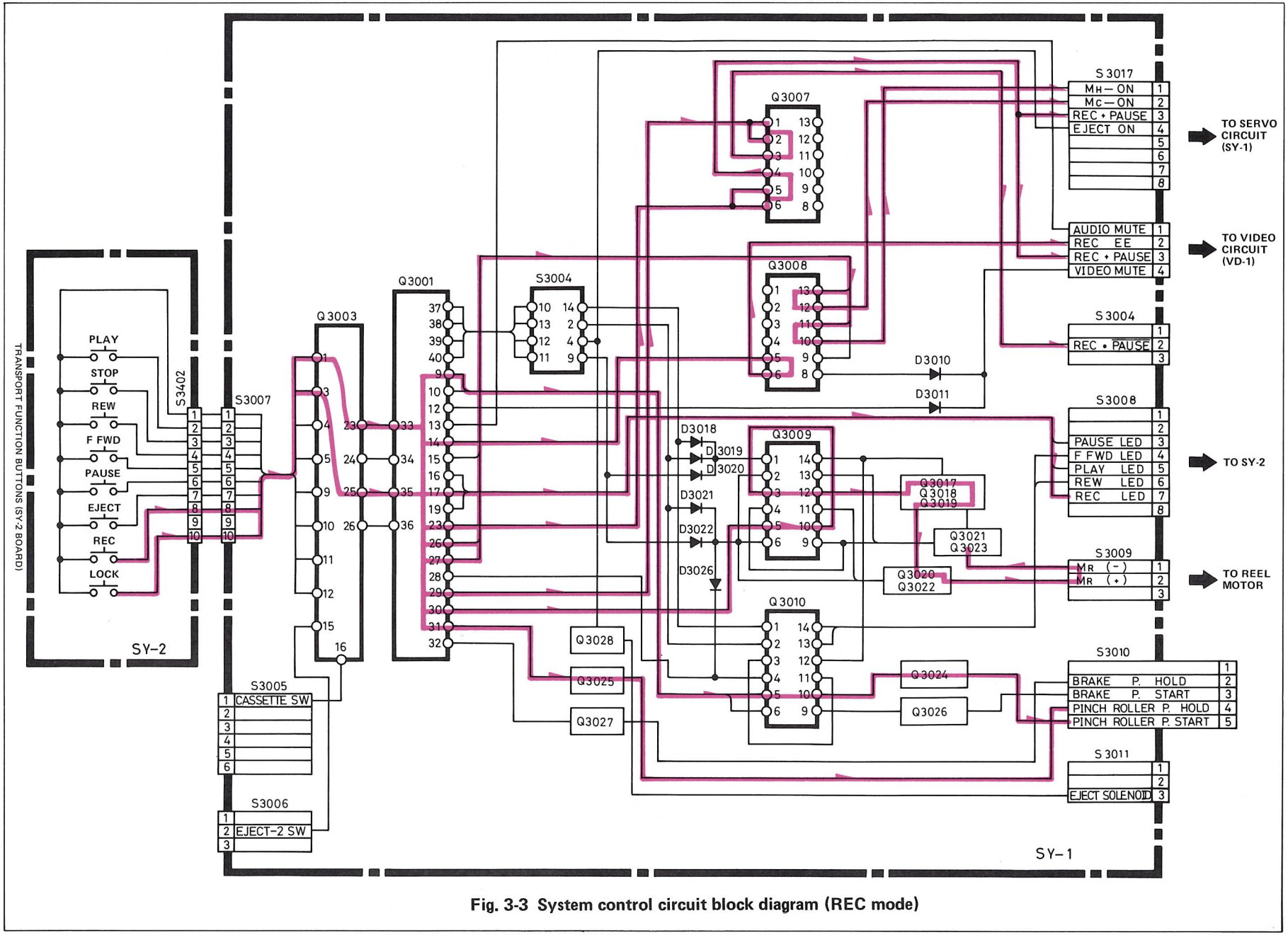


Fig. 3-3 System control circuit block diagram (REC mode)

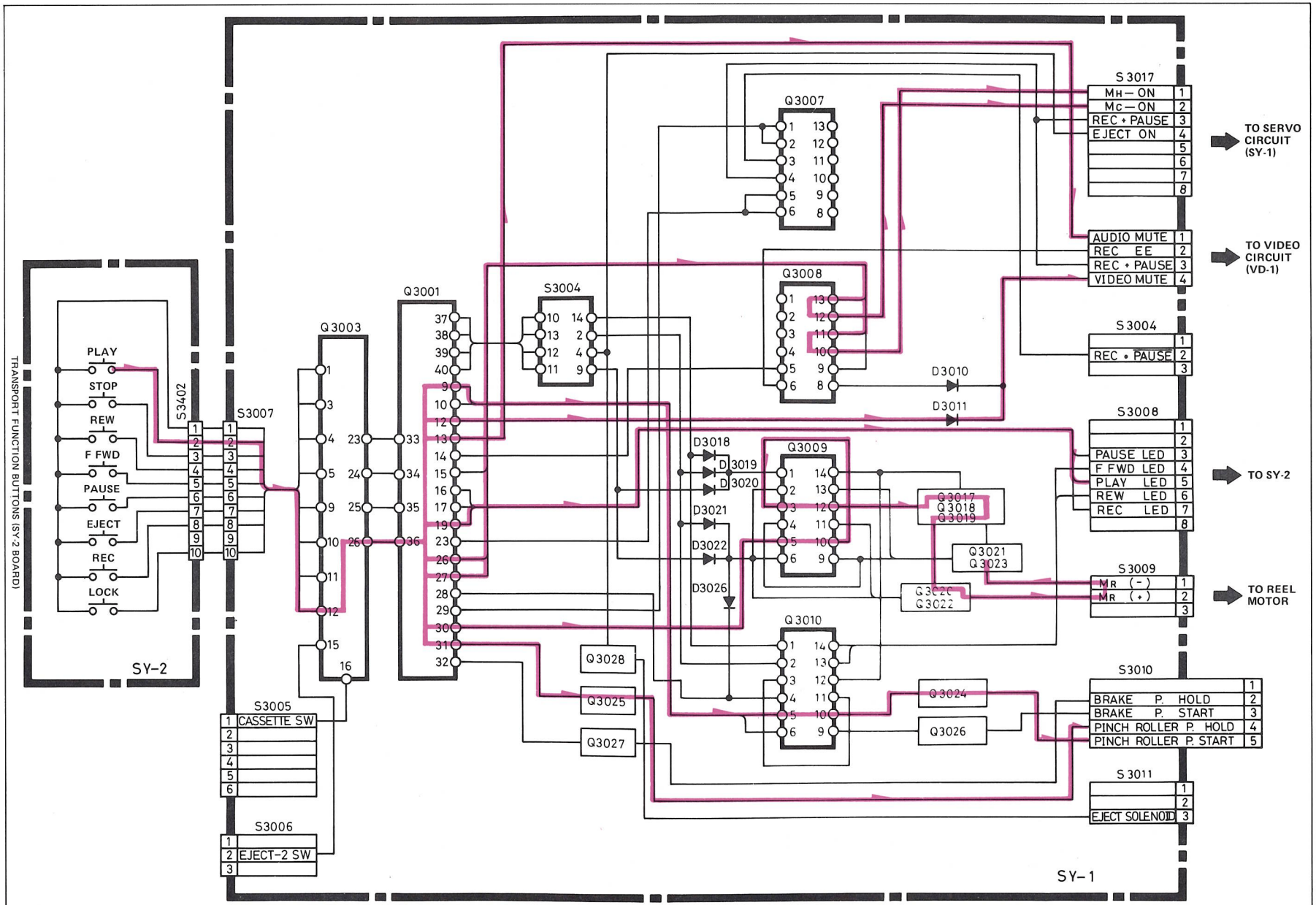


Fig. 3-4 System control circuit block diagram (PLAY mode)

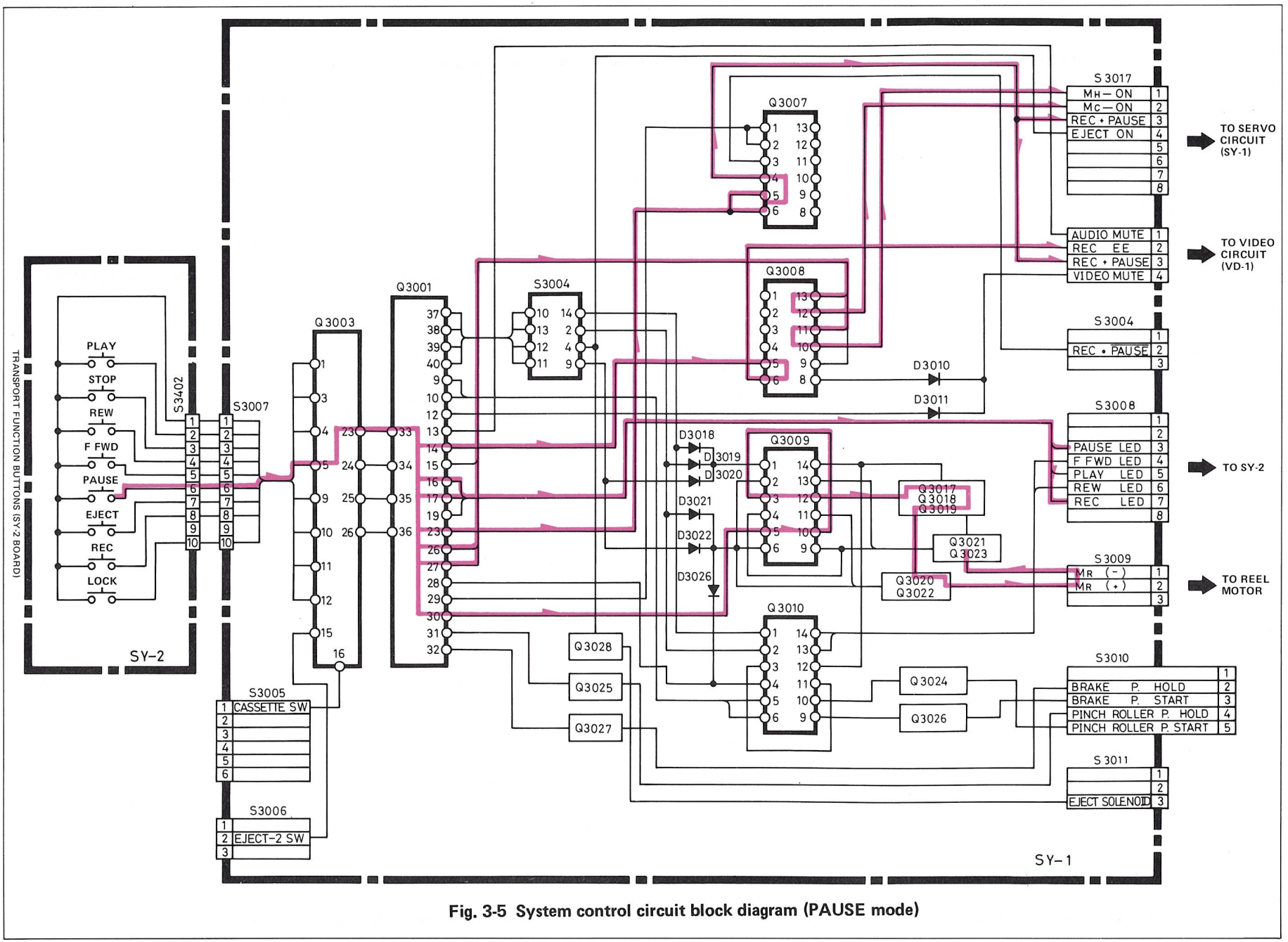


Fig. 3-5 System control circuit block diagram (PAUSE mode)

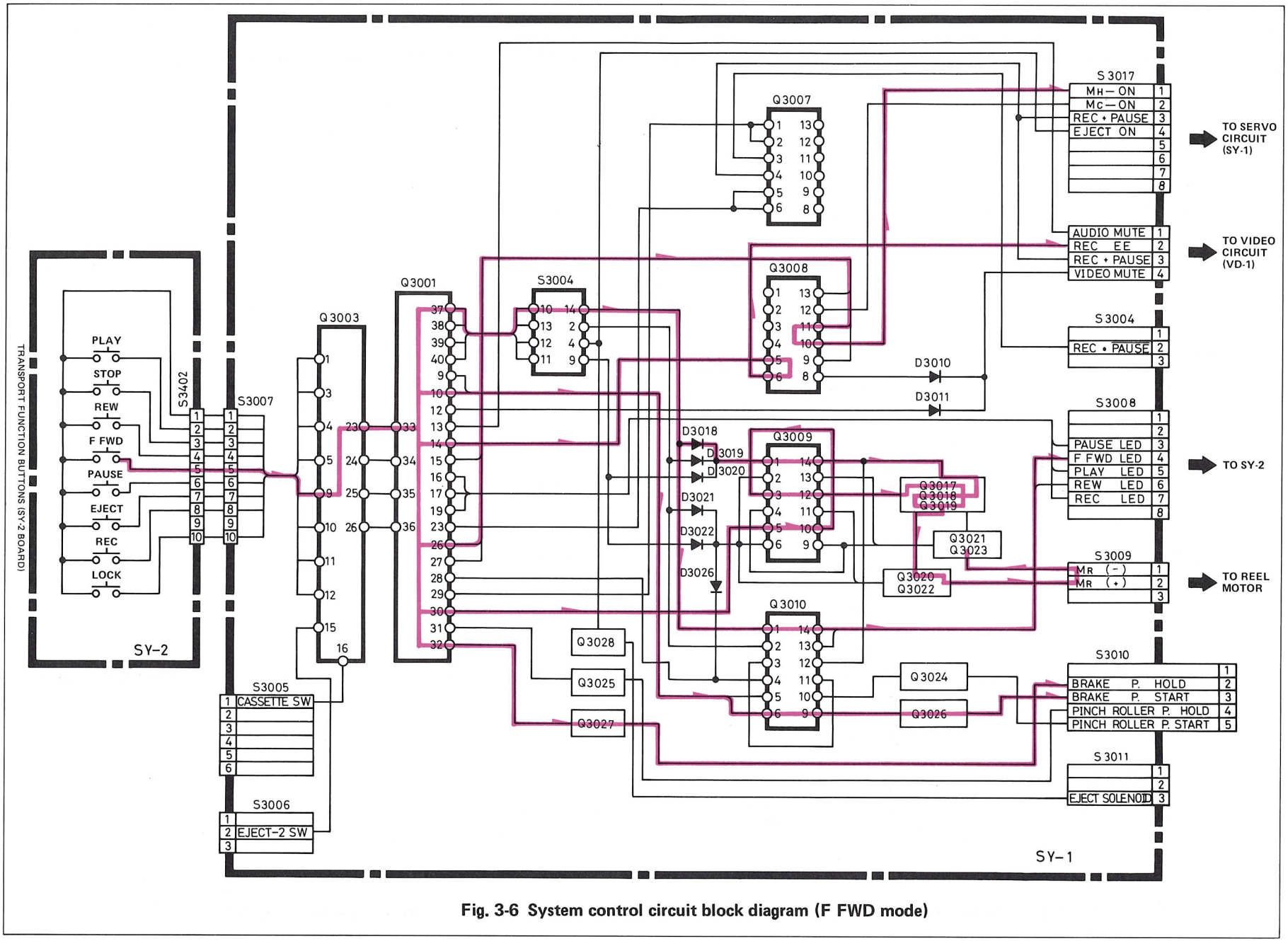


Fig. 3-6 System control circuit block diagram (F FWD mode)

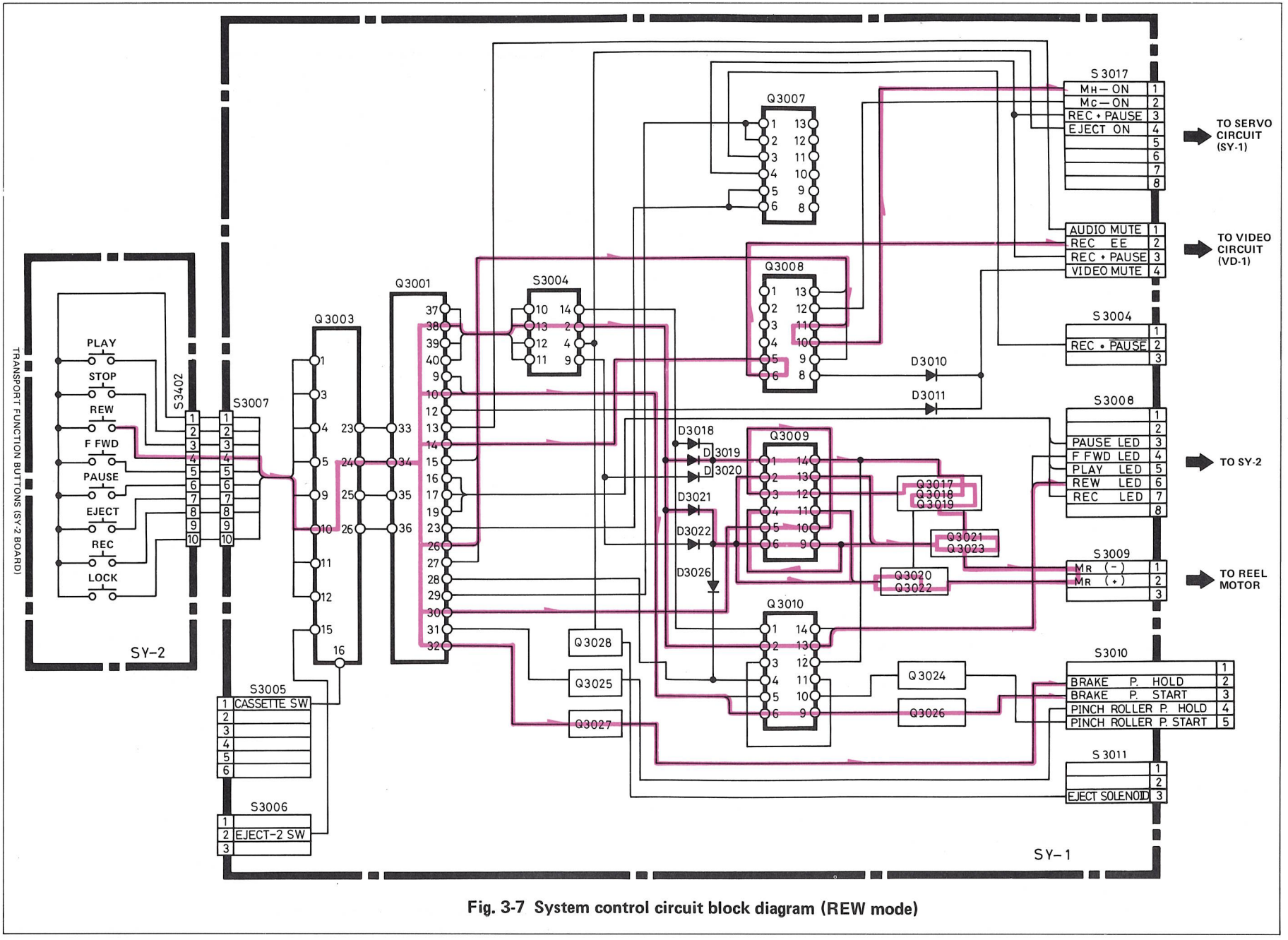


Fig. 3-7 System control circuit block diagram (REW mode)

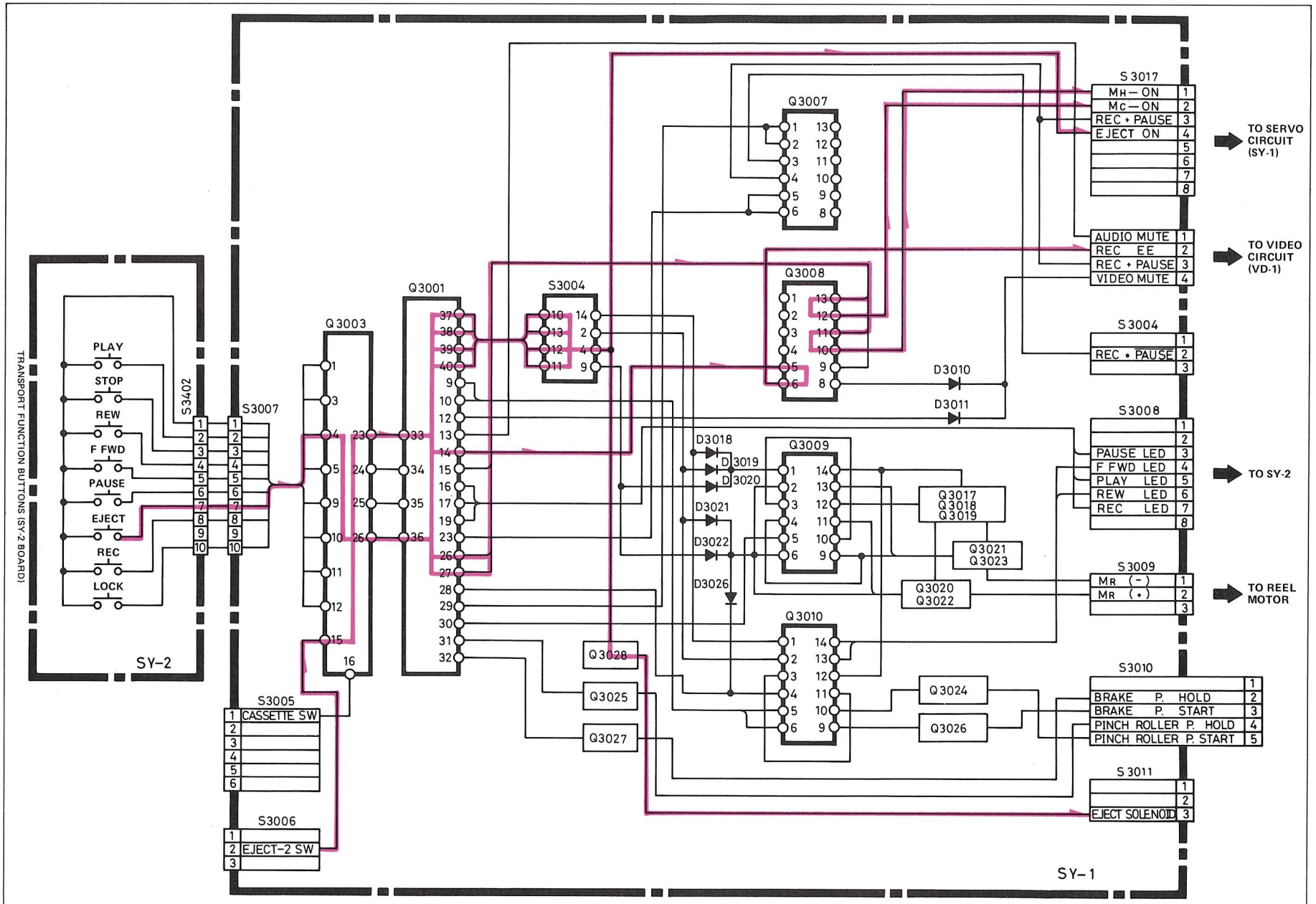


Fig. 3-8 System Control Circuit Block Diagram (UNLOADING mode)

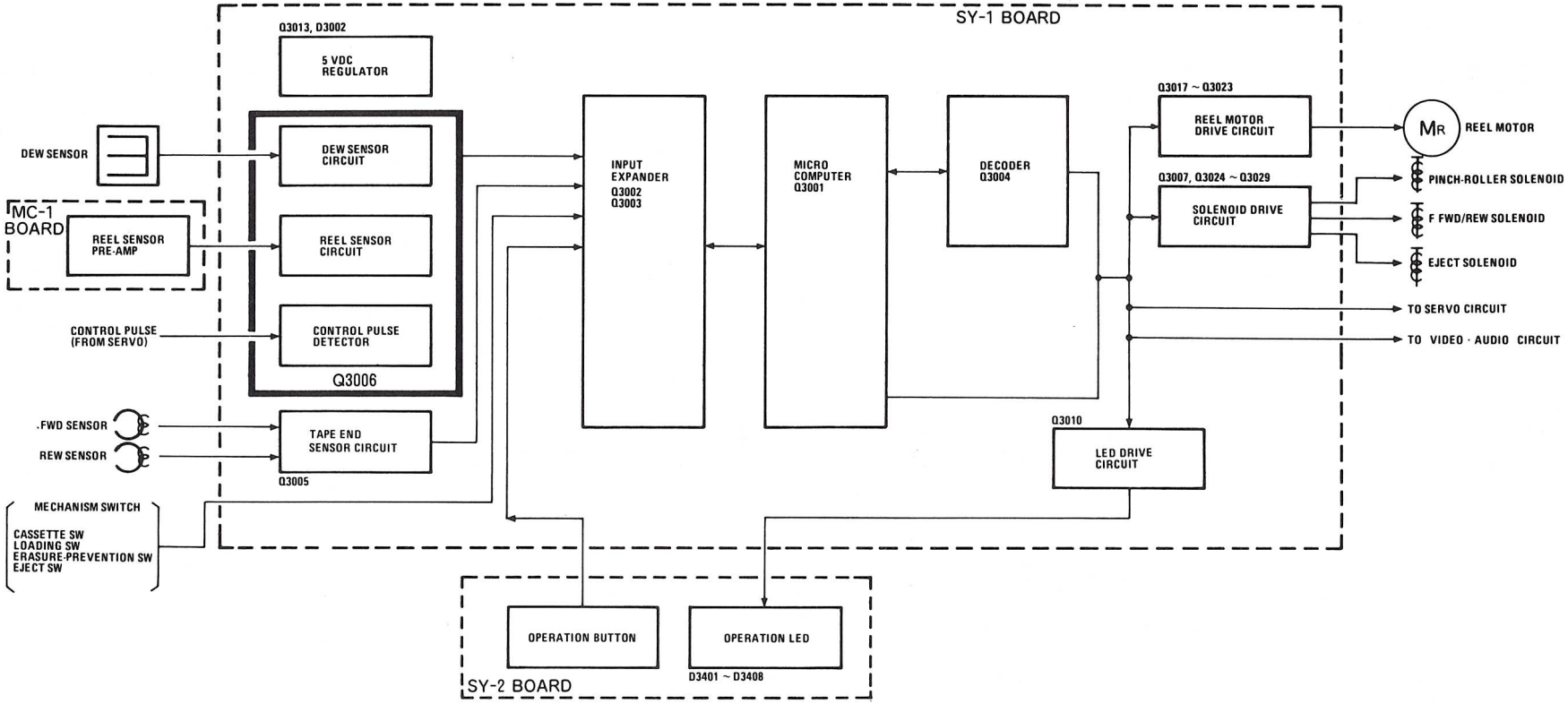


Fig. 3-9 System control circuit block diagram

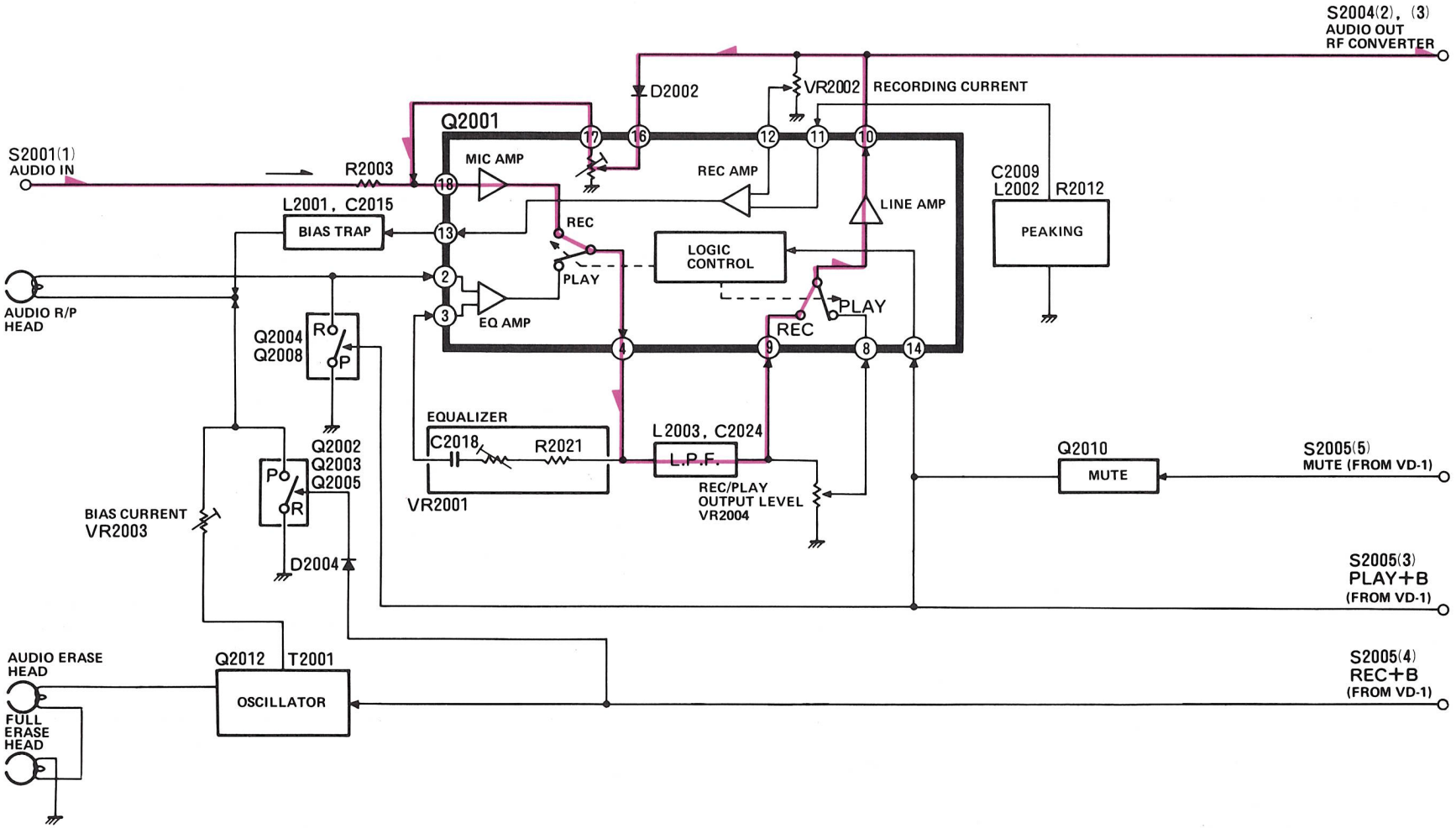


Fig. 4-1 Audio circuit block diagram (EE mode)

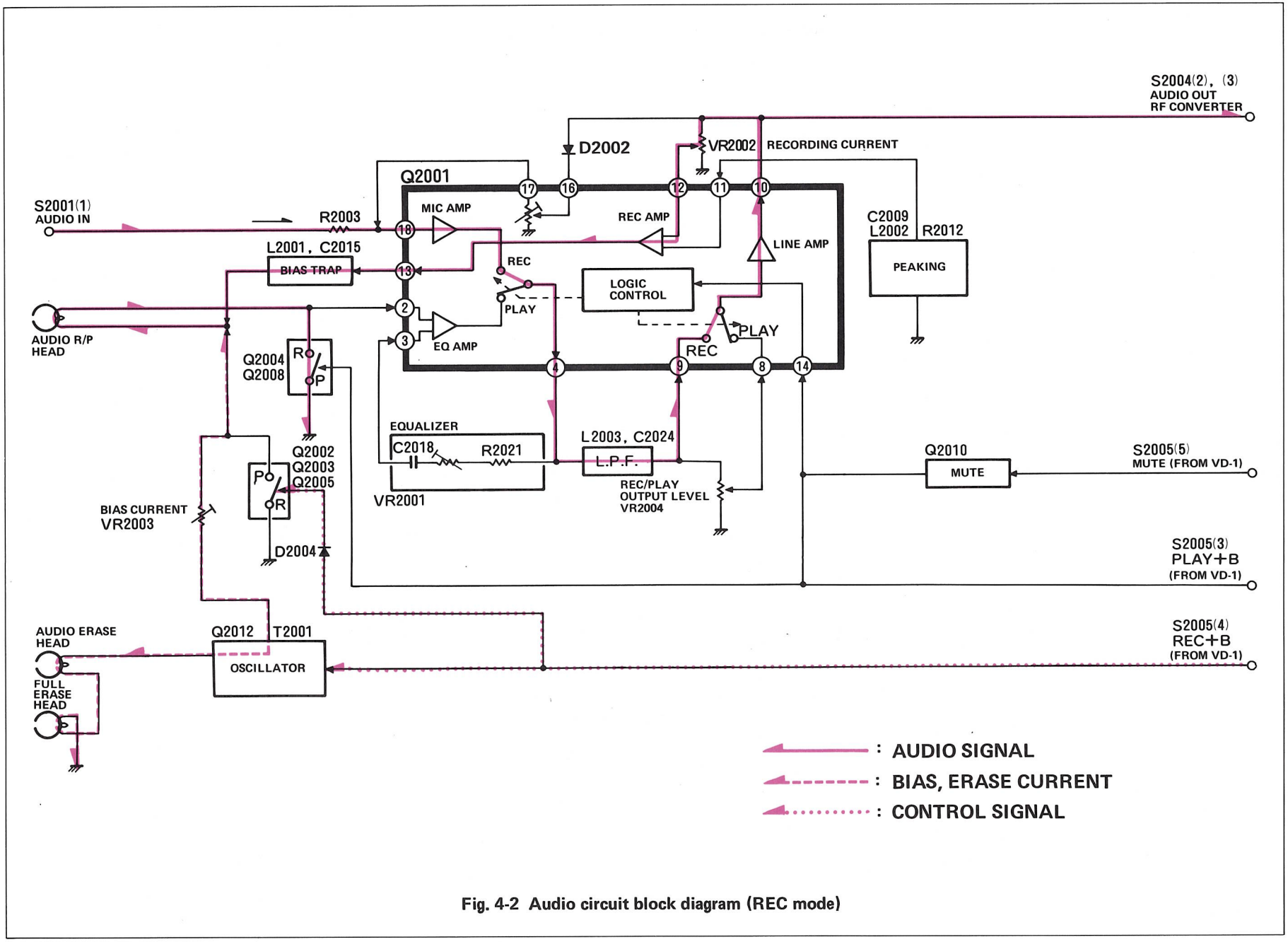


Fig. 4-2 Audio circuit block diagram (REC mode)

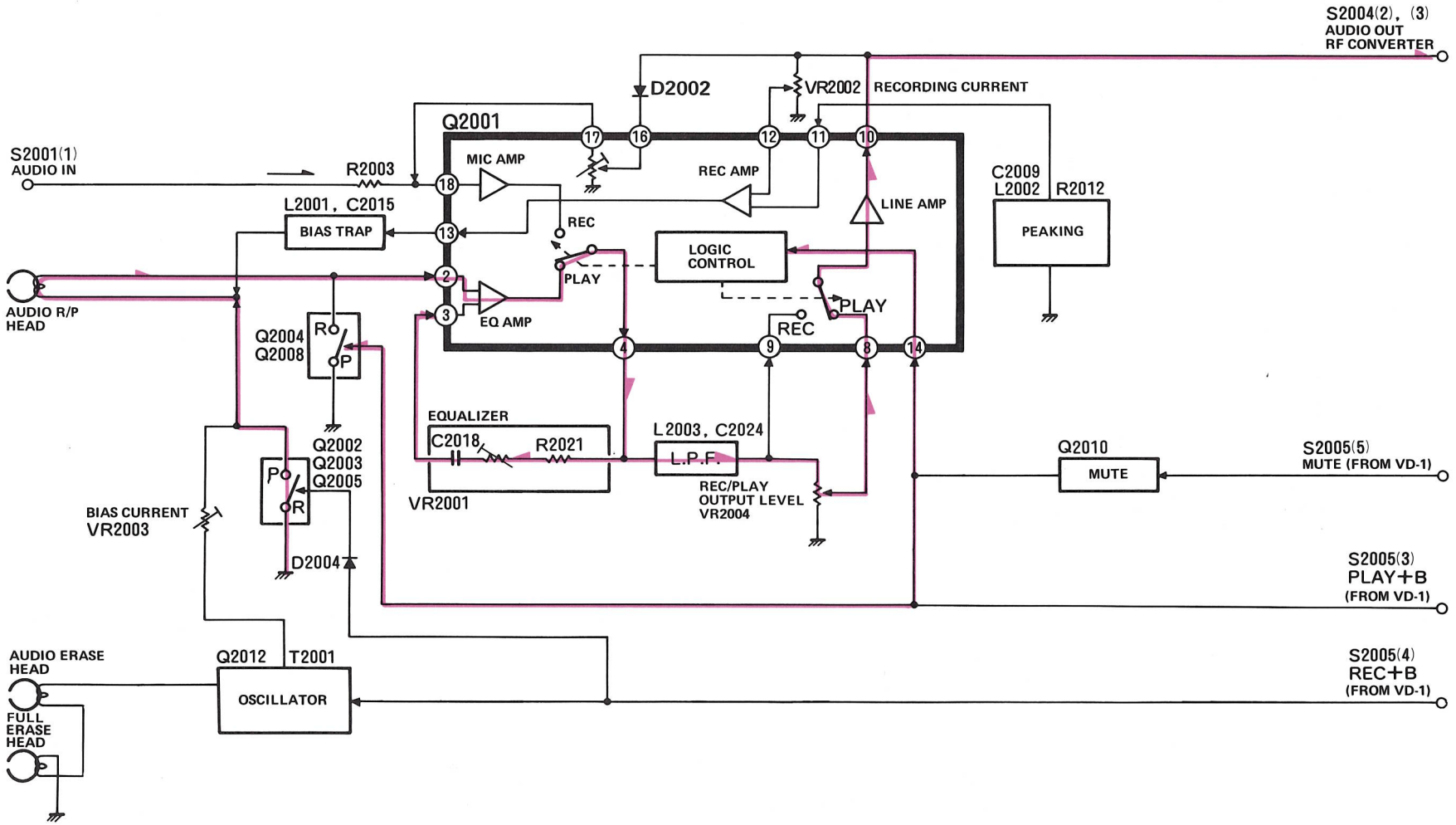


Fig. 4-3 Audio circuit block diagram (PLAY mode)

5. TIMER CIRCUIT

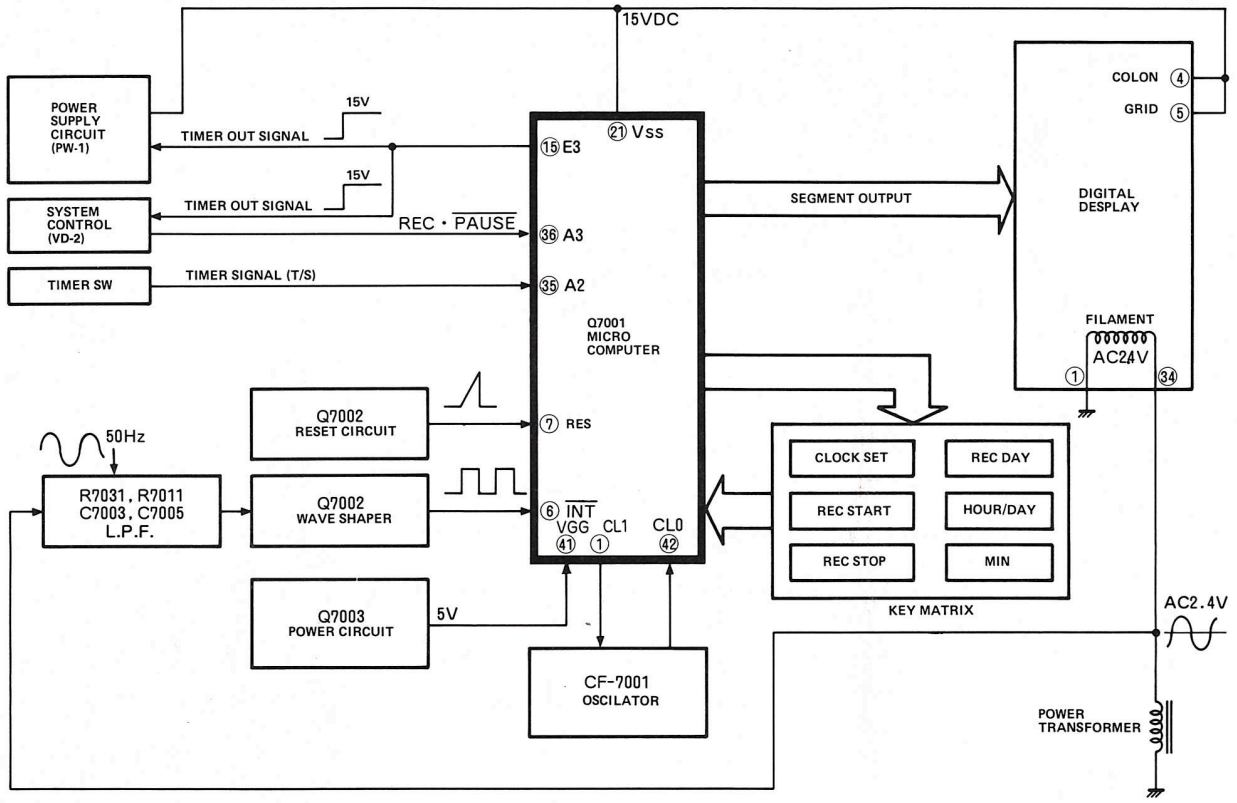


Fig. 5-1 Timer circuit block diagram

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